



UTG20N120LLS1

Insulated Gate Bipolar Transistor

1200V TRENCH GATE FIELD-STOP IGBT

■ DESCRIPTION

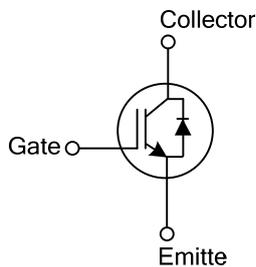
The UTC **UTG20N120LLS1** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

The UTC **UTG20N120LLS1** is suitable for the resonant or soft switching applications.

■ FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: $V_{CE(SAT),Typ.}=1.7V @ I_C=20A, V_{GE}=15V$
($T_C = 25^\circ C$)

■ SYMBOL

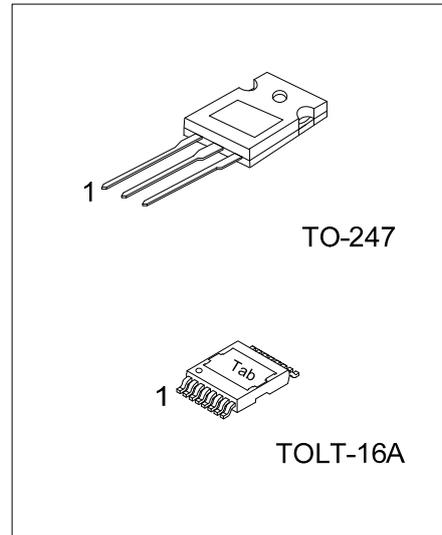


■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG20N120LLS1L-T47-T	UTG20N120LLS1G-T47-T	TO-247	G	C	E	Tube
UTG20N120LLS1L-TPA-R	UTG20N120LLS1G-TPA-R	TOLT-16A	refet to PIN CONFIGURATION			Tape Reel

Note: Pin Assignment: G: Gate C: Collector E: Emitter

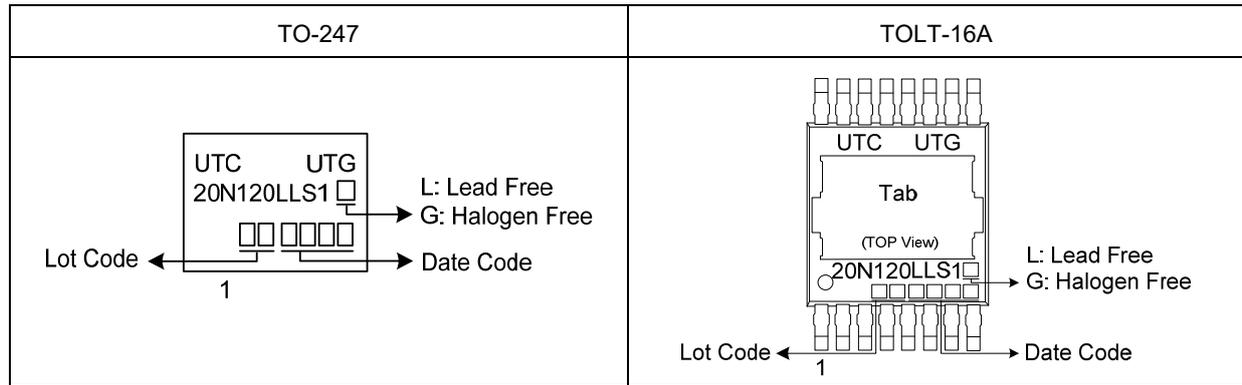
<p>UTG20N120LLS1G-T47-T</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) T47: TO-247, TPA: TOLT-16A</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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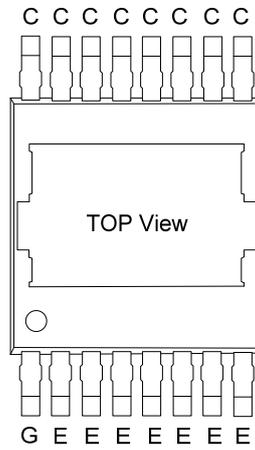
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MARKING



TOLT-16A PIN CONFIGURATION



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■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$, unless otherwise noted)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage	V_{CES}	1200	V
Gate-Emitter Voltage	V_{GES}	± 20	V
Transient Gate-emitter voltage ($t_p < 5$ ms)		± 25	V
Continuous Collector Current	I_C	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	A
Collector Current Pulsed (Note 1)	I_{CM}	80	A
Diode Forward Current	I_F	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	A
Short Circuit Withstand Time $V_{GE} = 15\text{V}$, $V_{CC} \leq 200\text{V}$ Allowed number of short circuits < 1000 Time between short circuits: $\geq 1.0\text{s}$ $T_{VJ} = 25^\circ\text{C}$	t_{SC}	8	μs
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D	TO-247	W
		TOLT-16A	W
Operating Junction Temperature	T_J	-40 ~ +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Absolute maximum ratings are those values beyond which the device could be permanently damaged.

2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θ_{JC}	TO-247	$^\circ\text{C/W}$
		TOLT-16A	$^\circ\text{C/W}$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

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■ ELECTRICAL CHARACTERISTICS (T_c=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS							
Collector-Emitter Breakdown Voltage	BV _{CES}		1200			V	
Collector Cut-Off Current	I _{CES}	V _{CE} =1200V, V _{GE} =0V			5	μA	
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V			±250	nA	
ON CHARACTERISTICS							
Gate to Emitter Threshold Voltage	V _{GE(TH)}	I _C =250μA, V _{CE} =V _{GE}	4.0		7.0	V	
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _C =20A, V _{GE} =15V	T _C =25°C	1.7	2.2	V	
			T _C =125°C	2.0		V	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{IES}	V _{CE} =25V, V _{GE} =0V, f=1MHz		1360		pF	
Output Capacitance	C _{OES}			74		pF	
Reverse Transfer Capacitance	C _{RES}			44		pF	
SWITCHING CHARACTERISTICS							
Total Gate Charge	Q _G	V _{CE} =600V, I _C =20A, V _{GE} =15V		116		nC	
Gate-Emitter Charge	Q _{GE}			14		nC	
Gate-Collector Charge	Q _{GC}			71		nC	
Turn-On Delay Time	t _{DON)}	V _{CC} =600V, I _C =20A, R _G =5Ω, V _{GE} =0~15V, L=500μH		11		ns	
Rise Time	t _R			19		ns	
Turn-Off Delay Time	t _{DOFF)}			143		ns	
Fall Time	t _F			271		ns	
Turn-On Switching Loss	E _{ON}			1.3		mJ	
Turn-Off Switching Loss	E _{OFF}			1.8		mJ	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Forward Voltage Drop	V _F		I _F =20A			2.5	V
Reverse Recovery Time	t _{rr}	I _F =20A, dI/dt=100A/μS, V _{CC} =600V		52		ns	
Reverse Recovery Charge	Q _{rr}			1600		nC	

■ TEST CIRCUIT AND WAVEFORMS

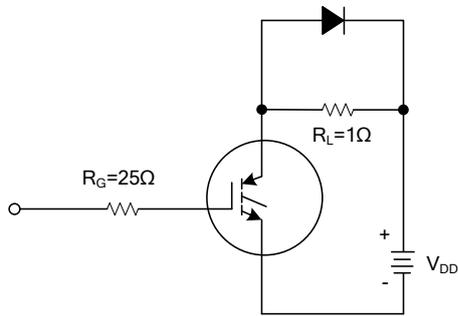


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

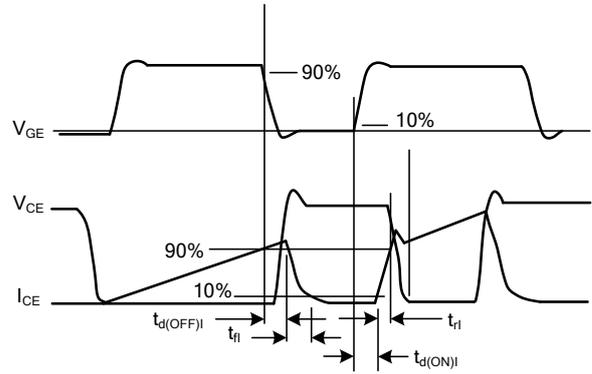
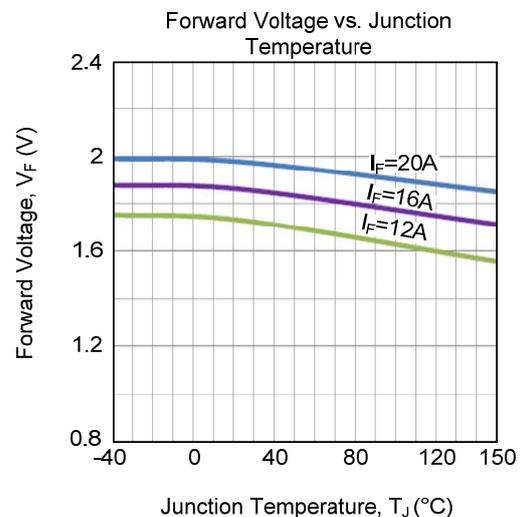
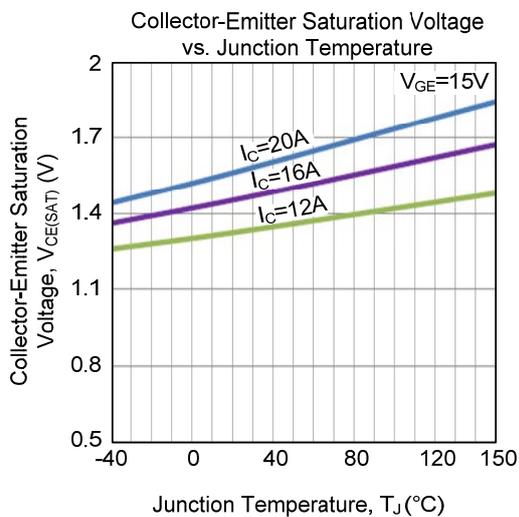
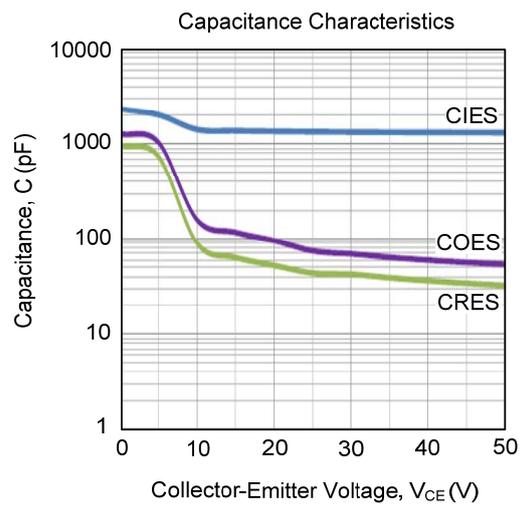
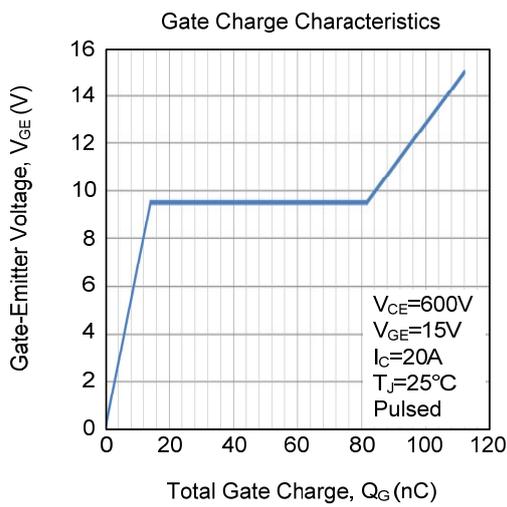
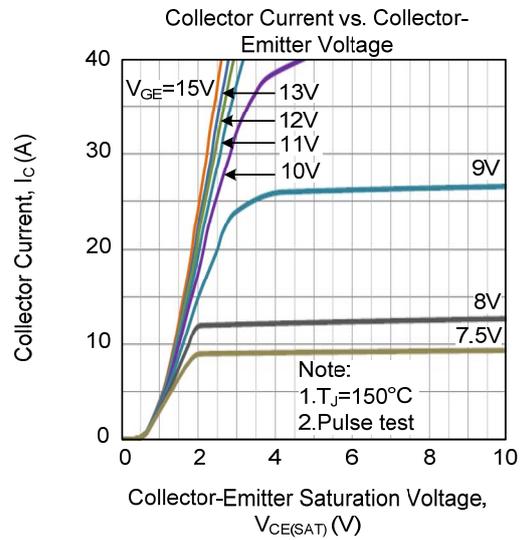
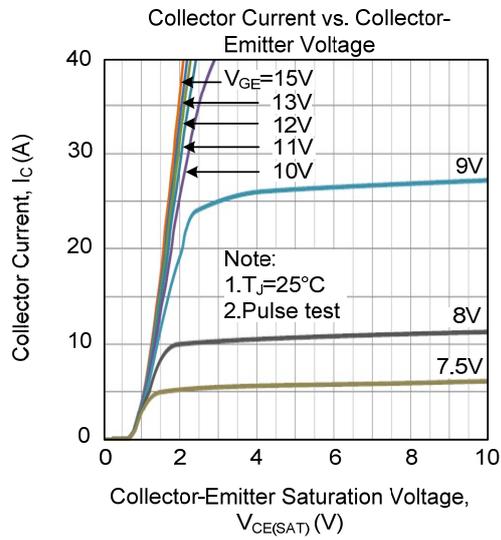
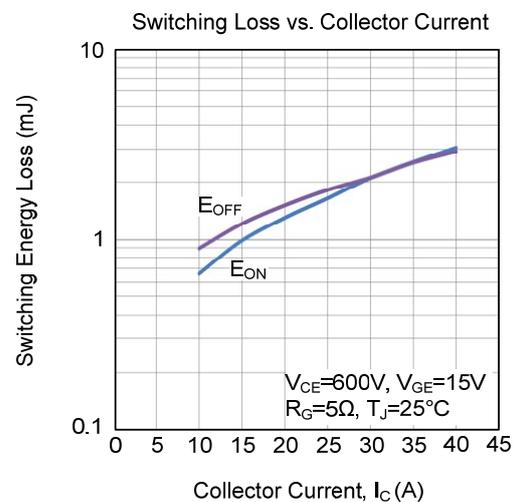
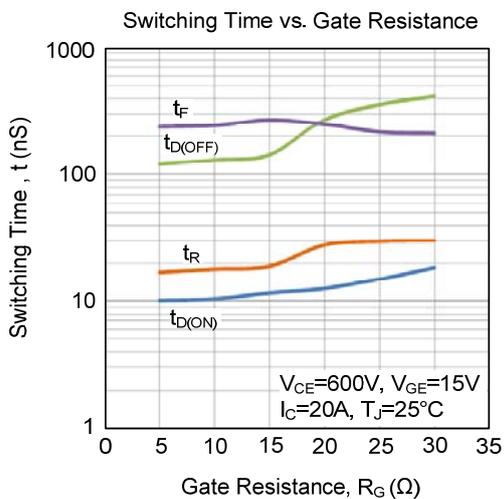
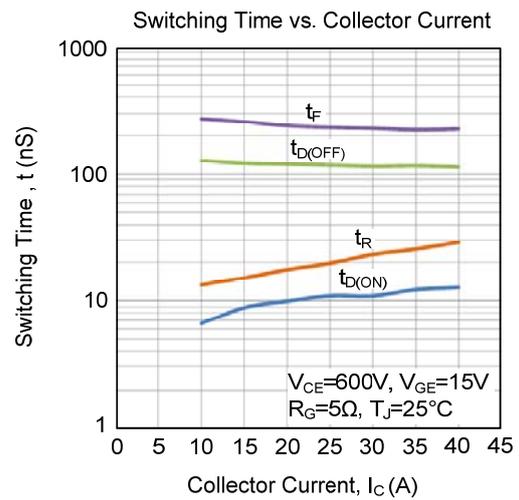
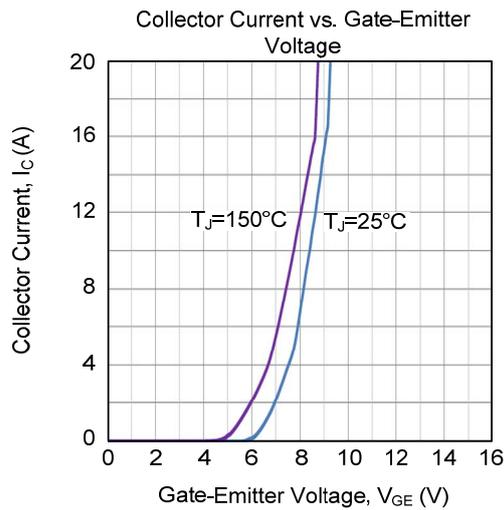
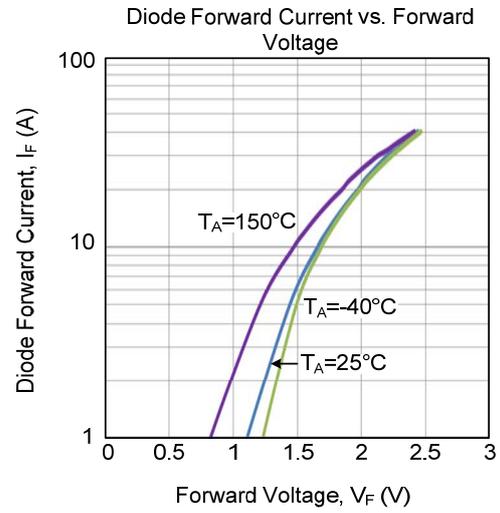
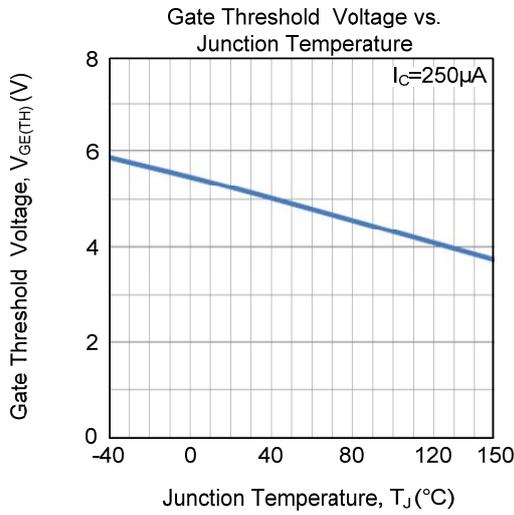


Fig 2. SWITCHING TEST WAVEFORMS

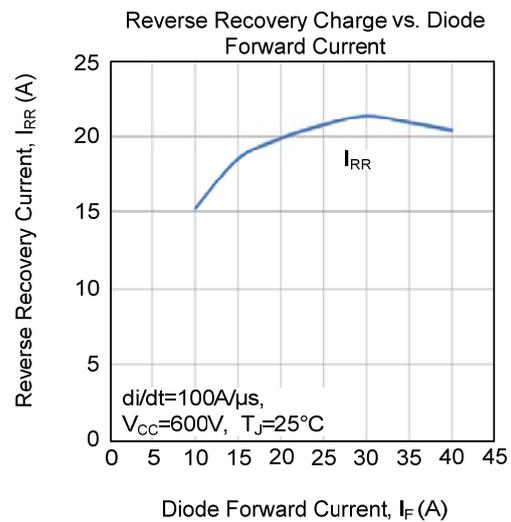
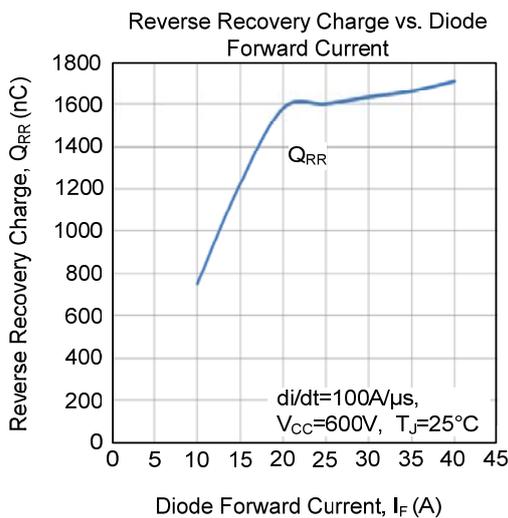
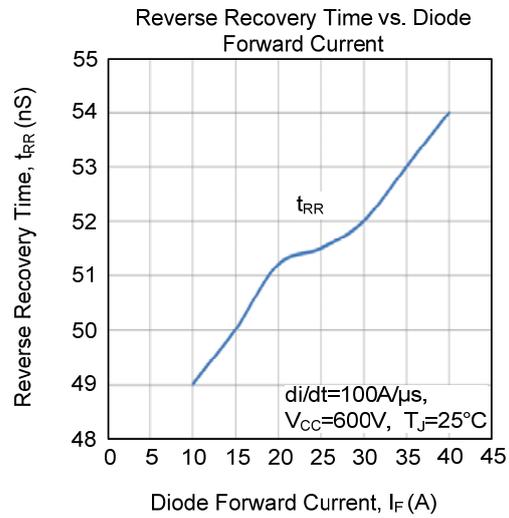
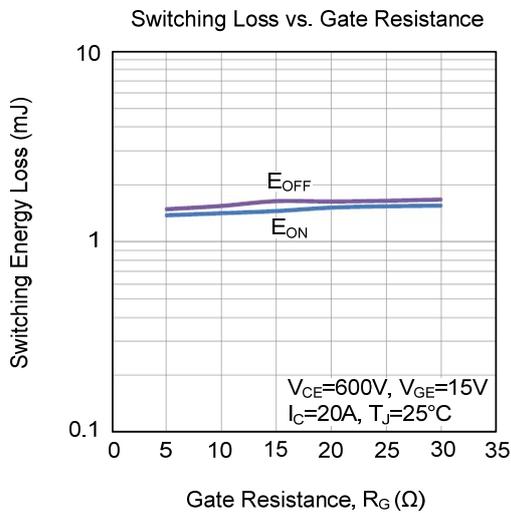
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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