



UT110P06H

Power MOSFET

-110A, -60V P-CHANNEL POWER MOSFET

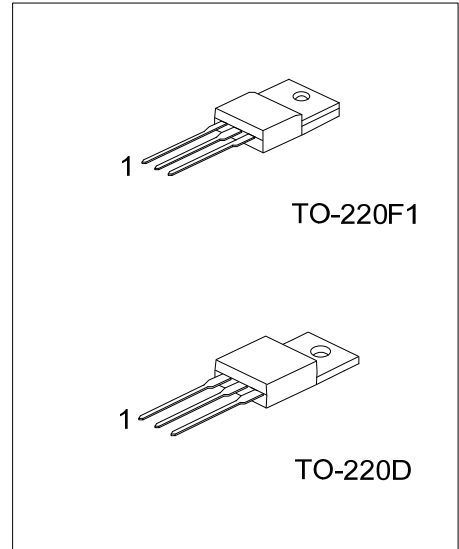
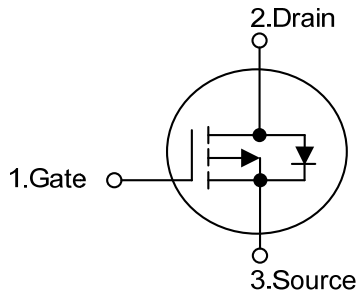
DESCRIPTION

The UTC **UT110P06H** provide excellent $R_{DS(ON)}$, low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

FEATURES

- * $R_{DS(ON)} \leq 6.5 \text{ m}\Omega$ @ $V_{GS} = -10\text{V}$, $I_D = -55\text{A}$
- * 100% Avalanche Tested

SYMBOL



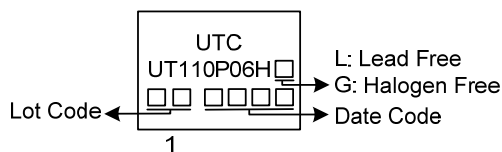
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT110P06HL-TAD-T	UT110P06HG-TAD-T	TO-220D	G	D	S	Tube
UT110P06HL-TF1-T	UT110P06HG-TF1-T	TO-220F1	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UT110P06HG-TAD-T</p> <p>(1)Packing Type (2)Package Type (3)Green Package</p>	<p>(1) T: Tube (2) TAD: TO-220D, TF1: TO-220F1 (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_c=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	-60	V
Gate-Source Voltage		V _{GSS}	±20	V
Drain Current	Continuous,	I _D	-110	A
	Pulsed (Note 2)	I _{DM}	-220	A
Avalanche Energy	Repetitive (Note 3)	E _{AS}	255	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	7.1	V/ns
Power Dissipation	TO-220F1	P _D	40	W
	TO-220D		215	W
Junction Temperature		T _J	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 0.1mH, I_{AS} = -71A, V_{DD} = -50V, R_G = 25Ω, Starting T_J = 25°C

4. I_{SD} ≤ -30A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220F1	θ _{JA}	62.5	°C/W
	TO-220D			°C/W
Junction to Case	TO-220F1	θ _{JC}	3.1	°C/W
	TO-220D		0.58	°C/W

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

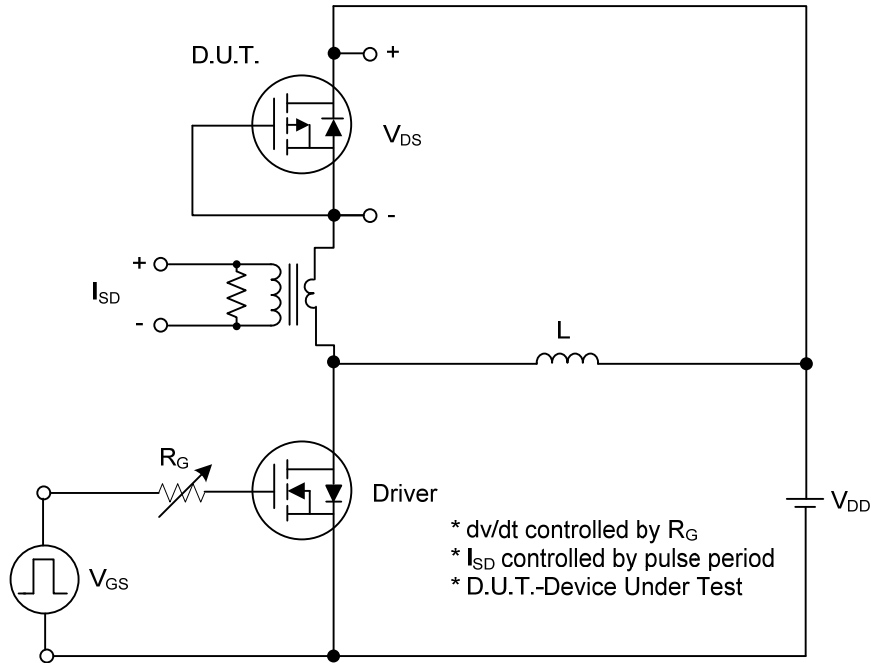
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =-250μA, V _{GS} =0V	-60			V	
Drain-Source Leakage Current	I _{DSS}	V _{DS} =-60V, V _{GS} =0V			-1	μA	
Gate- Source Leakage Current	Forward	I _{GSS}			+100	nA	
	Reverse						V _{GS} =+20V, V _{DS} =0V
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250μA	-2.0		-4.0	V	
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-55A			6.5	mΩ	
DYNAMIC PARAMETERS							
Input Capacitance	C _{ISS}	V _{DS} =-25V, V _{GS} =0V, f=1MHz		11.68		nF	
Output Capacitance	C _{OSS}				1019		pF
Reverse Transfer Capacitance	C _{RSS}				758		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)	Q _G	V _{DS} =-48V, V _{GS} =-10V, I _D =-110A (Note 1, 2)		228		nC	
Gate to Source Charge	Q _{GS}				52		nC
Gate to Drain Charge	Q _{GD}				70		nC
Turn-ON Delay Time (Note 1)	t _{D(ON)}	V _{DD} =-30V, I _D =-110A, R _G =3Ω (Note 1, 2)		32		ns	
Rise Time	t _R				30		ns
Turn-OFF Delay Time	t _{D(OFF)}				141		ns
Fall-Time	t _F				69		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current	I _S				-110	A	
Drain-Source Diode Forward Voltage	V _{SD}	I _S =-110A, V _{GS} =0V (Note 2)			-1.4	V	
Body Diode Reverse Recovery Time (Note 1)	t _{rr}	I _S =-30A, V _{GS} =0V, dI _F /dt=100A/μs		117		ns	
Body Diode Reverse Recovery Charge	Q _{rr}				324		nC

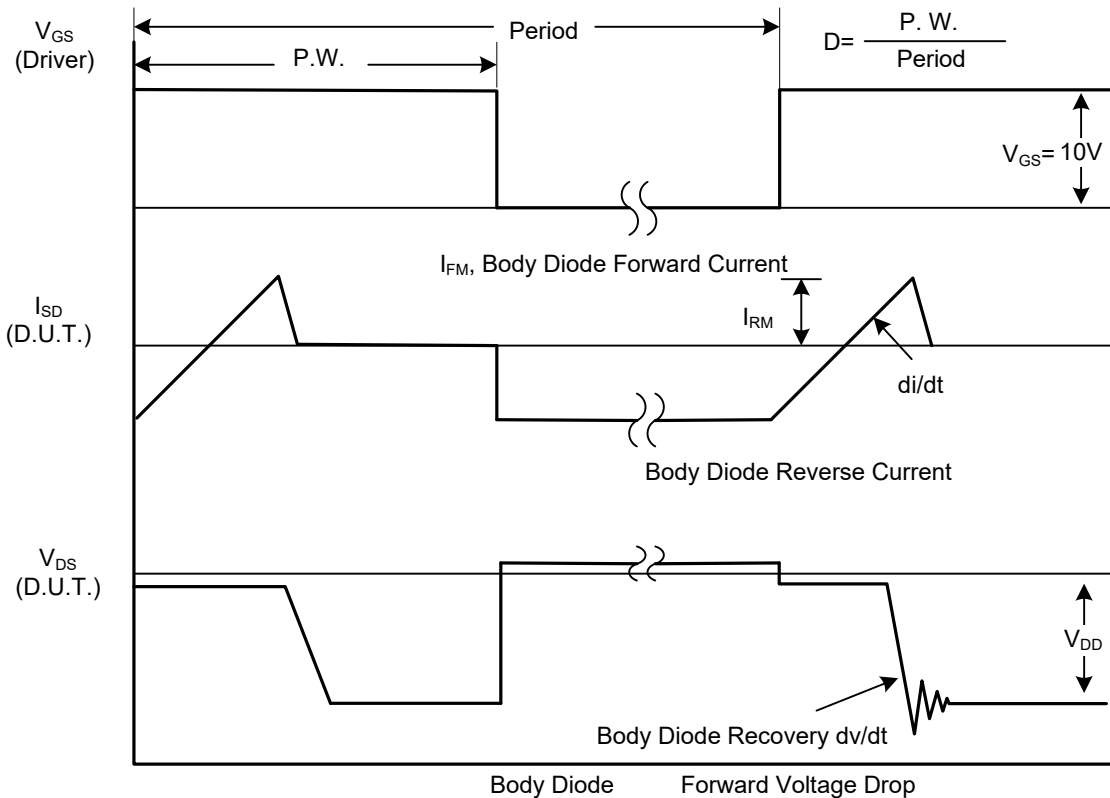
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating ambient temperature.

TEST CIRCUITS AND WAVEFORMS

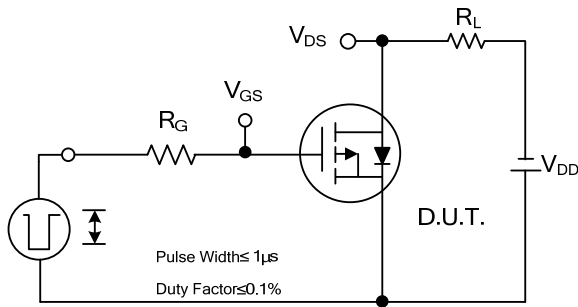


Peak Diode Recovery dv/dt Test Circuit

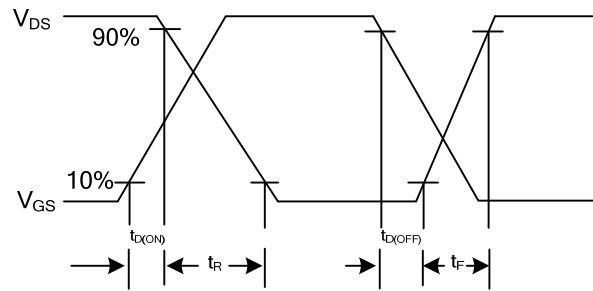


Peak Diode Recovery dv/dt Waveforms

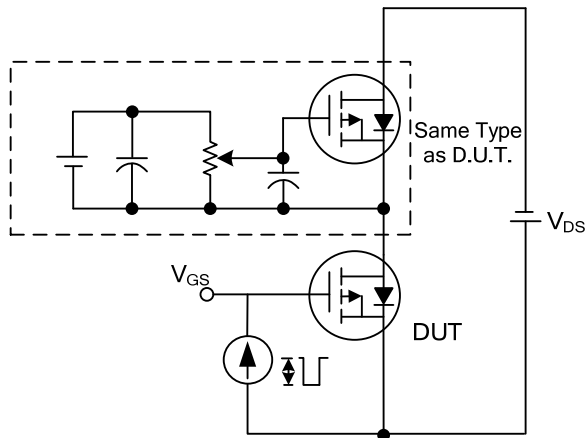
TEST CIRCUITS AND WAVEFORMS



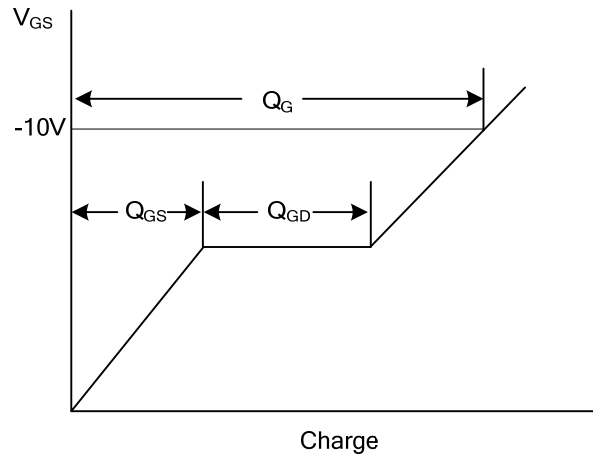
Switching Test Circuit



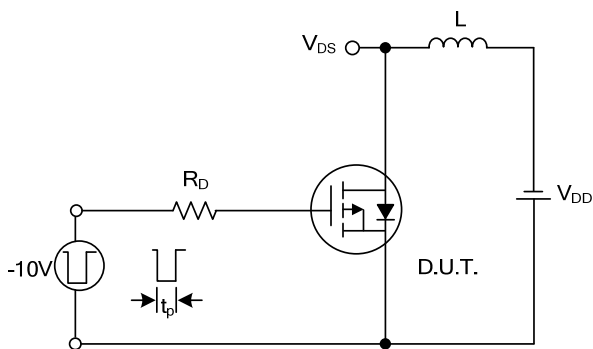
Switching Waveforms



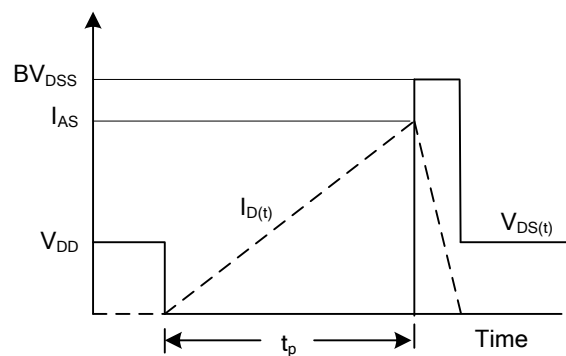
Gate Charge Test Circuit



Gate Charge Waveform

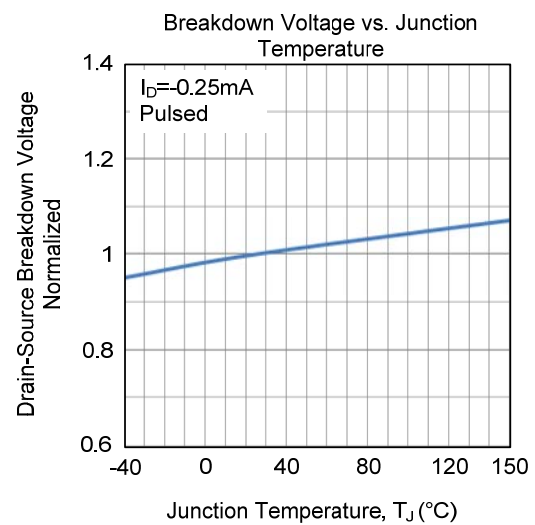
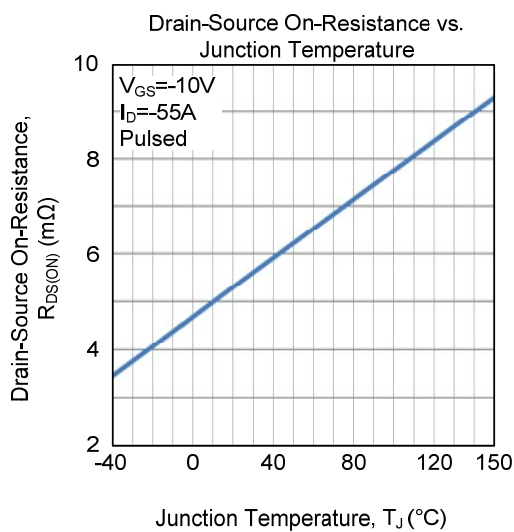
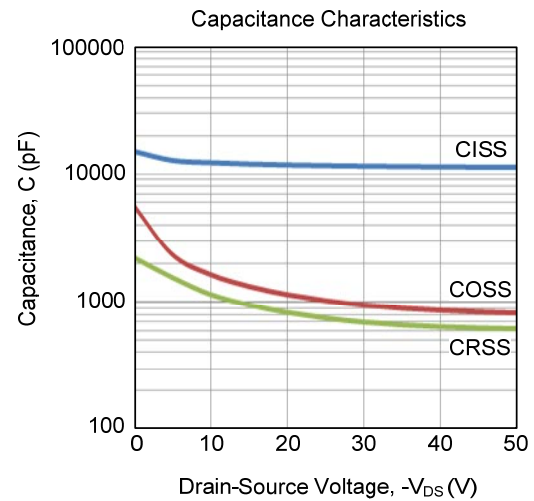
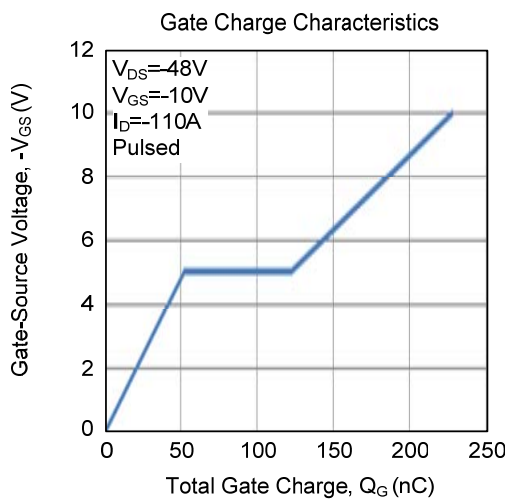
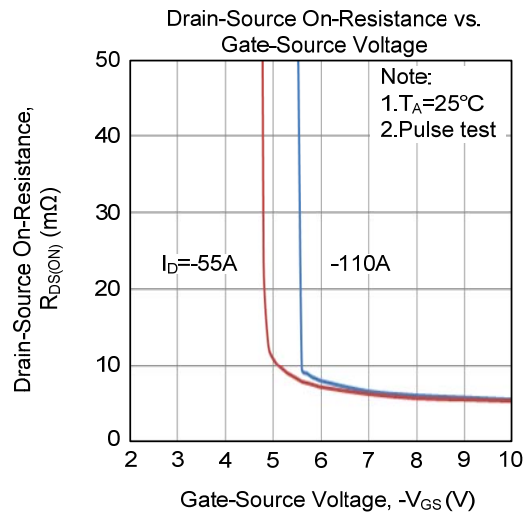
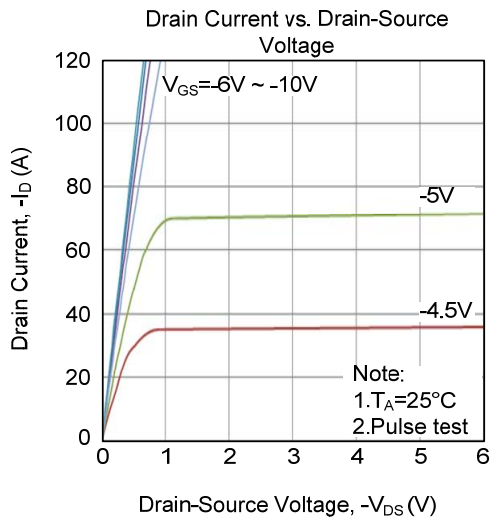


Unclamped Inductive Switching Test Circuit

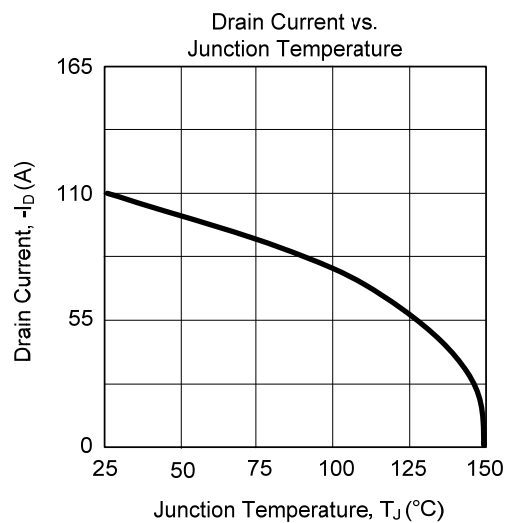
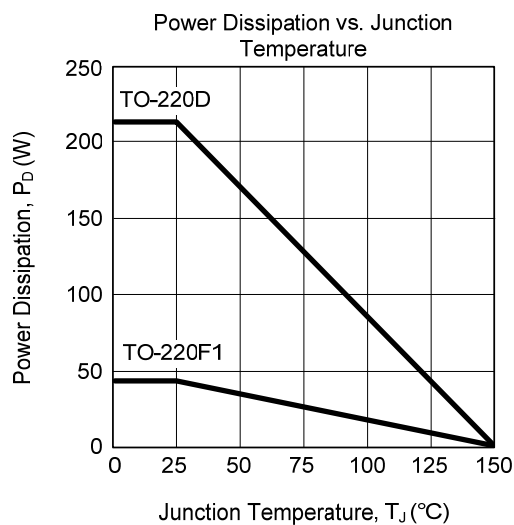
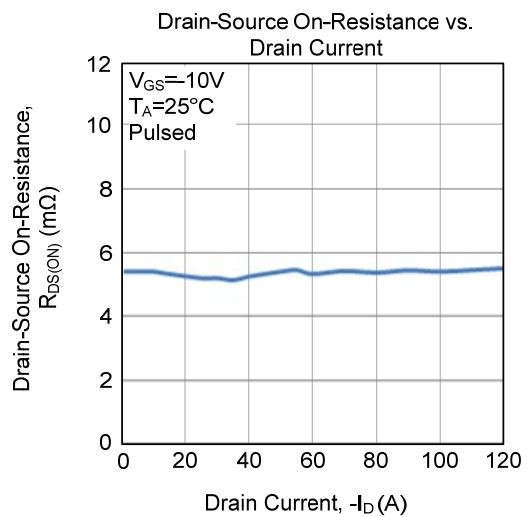
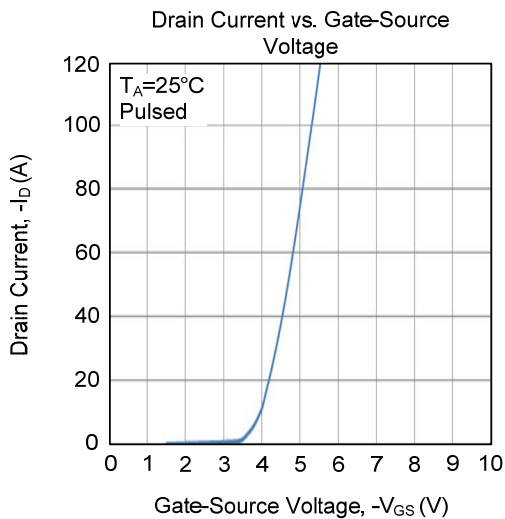
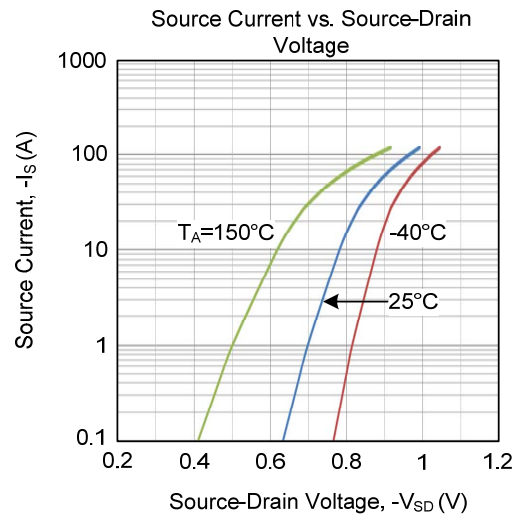
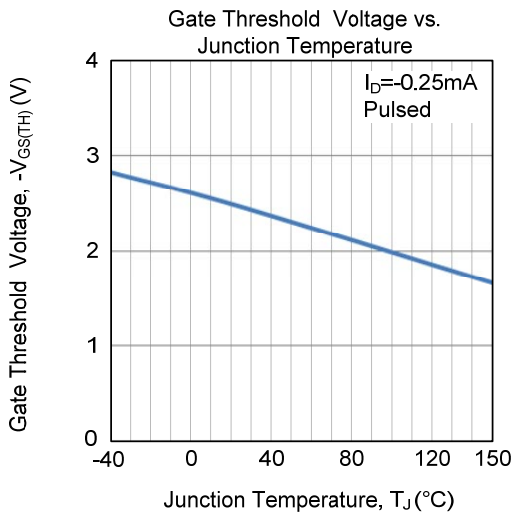


Unclamped Inductive Switching Waveforms

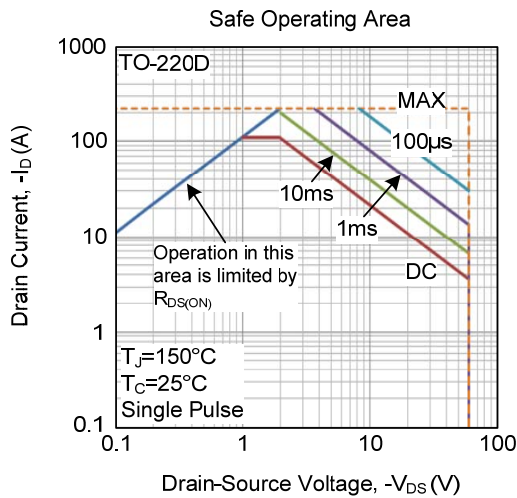
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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