



# UTG20N65HUD1

*Insulated Gate Bipolar Transistor*

## 650V TRENCH GATE FIELD-STOP IGBT

### DESCRIPTION

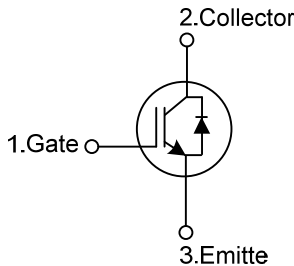
The UTC **UTG20N65HUD1** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

The UTC **UTG20N65HUD1** is suitable for the resonant or soft switching applications.

### FEATURES

- \* High switching speed
- \* High avalanche ruggedness
- \* Low saturation voltage:  $V_{CE(SAT),Typ.} = 1.7V @ I_C=20A, V_{GE}=15V$  ( $T_C = 25^\circ C$ )

### SYMBOL

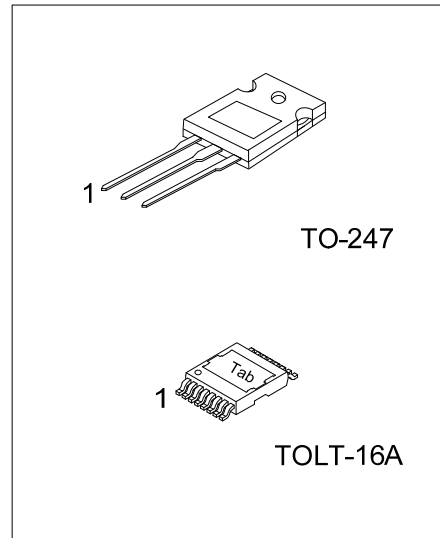


### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG20N65HUD1L-T47-T	UTG20N65HUD1G-T47-T	TO-247	G	C	E	Tube
UTG20N65HUD1L-TPA-R	UTG20N65HUD1G-TPA-R	TOLT-16A	refet to PIN CONFIGURATION			Tape Reel

Note: Pin Assignment: G: Gate C: Collector E: Emitter

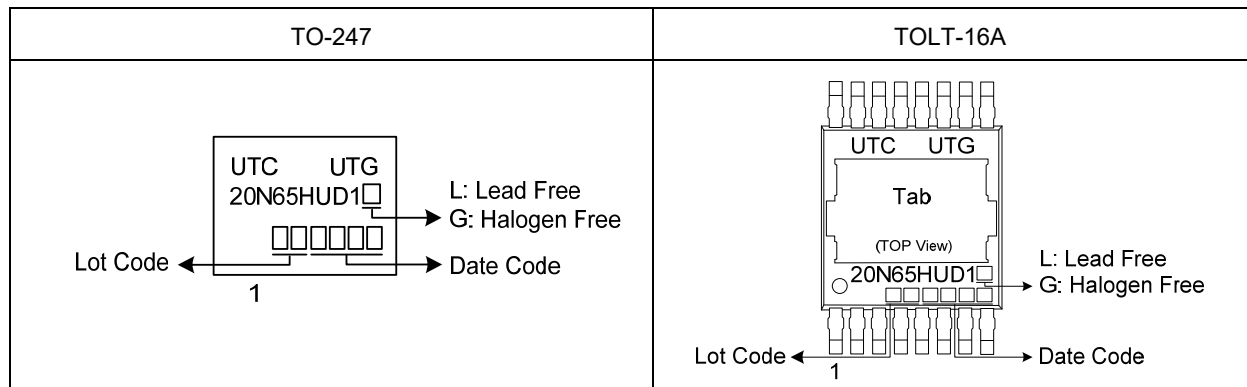
<p>UTG20N65HUD1G-T47-T</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) T47: TO-247, TPA: TOLT-16A</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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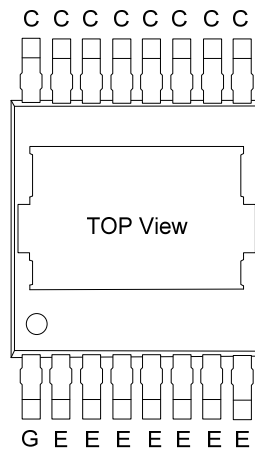
# UTG20N65HUD1

## Insulated Gate Bipolar Transistor

### MARKING



### TOLT-16A PIN CONFIGURATION



■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage	V <sub>CES</sub>	650	V	
Gate-Emitter Voltage	V <sub>GES</sub>	±20	V	
Transient Gate-Emitter Voltage (t <sub>p</sub> < 5 ms)		±25	V	
Continuous Collector Current	I <sub>C</sub>	T <sub>C</sub> =25°C	40	A
		T <sub>C</sub> =100°C	20	A
Collector Current Pulsed (Note 1)	I <sub>CM</sub>	120	A	
Diode Forward Current	I <sub>F</sub>	T <sub>C</sub> =25°C	40	A
		T <sub>C</sub> =100°C	20	A
Short Circuit Withstand Time V <sub>GE</sub> = 15V, V <sub>CC</sub> ≤ 200V Allowed number of short circuits < 1000 Time between short circuits: ≥ 1.0s T <sub>VJ</sub> = 25°C	t <sub>SC</sub>	3	μs	
Power Dissipation (T <sub>C</sub> =25°C)	TO-247	200	W	
	TOLT-16A	110	W	
Operating Junction Temperature	T <sub>J</sub>	-40 ~ +150	°C	
Storage Temperature Range	T <sub>STG</sub>	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Absolute maximum ratings are those values beyond which the device could be permanently damaged.

2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Case	θ <sub>JC</sub>	TO-247	0.62	°C/W
		TOLT-16A	1.13 (Note)	°C/W

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
<b>Off Characteristics</b>							
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>		650			V	
Collector Cut-Off Current	I <sub>CES</sub>	V <sub>CE</sub> =650V, V <sub>GE</sub> =0V			5	μA	
G-E Leakage Current	I <sub>GES</sub>	V <sub>CE</sub> =0V, V <sub>GE</sub> =±20V			±400	nA	
<b>On Characteristics</b>							
Gate to Emitter Threshold Voltage	V <sub>GE(TH)</sub>	I <sub>C</sub> =250μA, V <sub>CE</sub> =V <sub>GE</sub>	3.5		6.5	V	
Collector to Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =20A, V <sub>GE</sub> =15V	T <sub>C</sub> =25°C	1.7	2.2	V	
			T <sub>C</sub> =125°C	2.1		V	
<b>Dynamic Characteristics</b>							
Input Capacitance	C <sub>IES</sub>	V <sub>CE</sub> =25V, V <sub>GE</sub> =0V, f=1MHz		937		pF	
Output Capacitance	C <sub>OES</sub>			71		pF	
Reverse Transfer Capacitance	C <sub>RES</sub>			14		pF	
<b>Switching Characteristics</b>							
Total Gate Charge	Q <sub>G</sub>	V <sub>CE</sub> =520V, I <sub>C</sub> =20A, V <sub>GE</sub> =15V		53		nC	
Gate-Emitter Charge	Q <sub>GE</sub>			20		nC	
Gate-Collector Charge	Q <sub>GC</sub>			20		nC	
Turn-On Delay Time	t <sub>DON)</sub>	V <sub>CC</sub> =400V, I <sub>C</sub> =20A, R <sub>G</sub> =5Ω, V <sub>GE</sub> =0~15V, L=500μH		10		ns	
Rise Time	t <sub>R</sub>			22		ns	
Turn-Off Delay Time	t <sub>DOFF)</sub>			32		ns	
Fall Time	t <sub>F</sub>			166		ns	
Turn-On Switching Loss	E <sub>ON</sub>				0.9		mJ
Turn-Off Switching Loss	E <sub>OFF</sub>				0.4		mJ
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>							
Forward Voltage Drop	V <sub>F</sub>		I <sub>F</sub> =20A			2.3	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =20A, dI/dt=100A/μS		41		ns	
Reverse Recovery Charge	Q <sub>rr</sub>				179		nC

### ■ TEST CIRCUIT AND WAVEFORMS

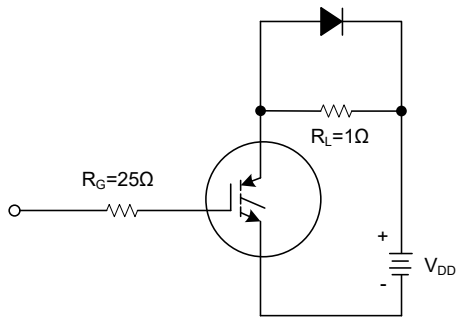


Figure 1. INDUCTIVE SWITCHING TEST CIRCUIT

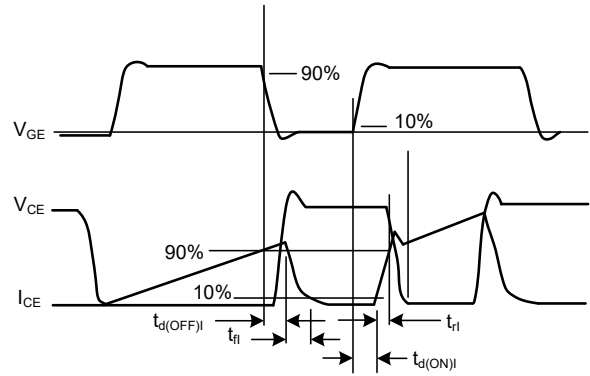
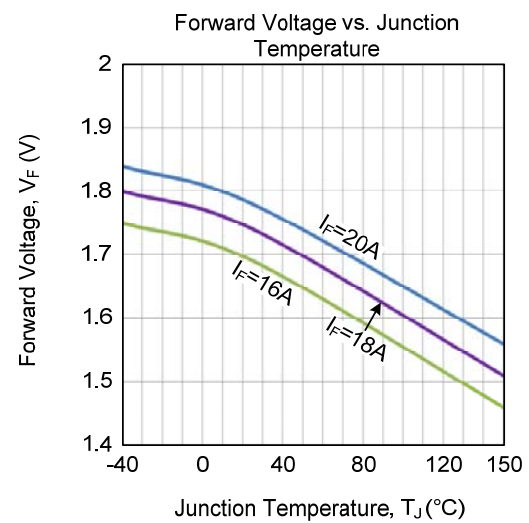
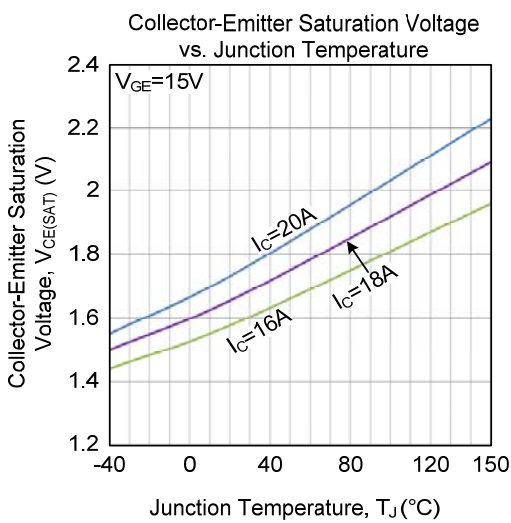
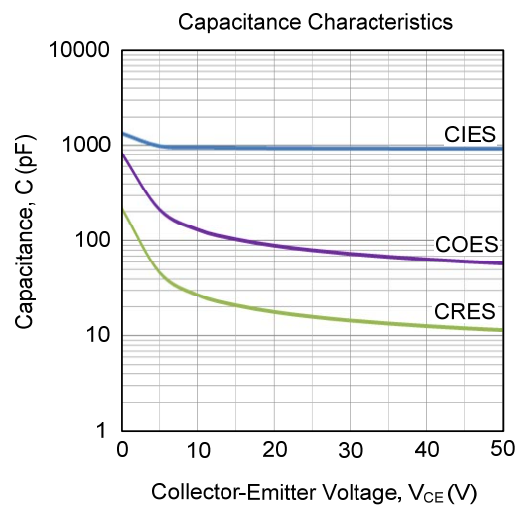
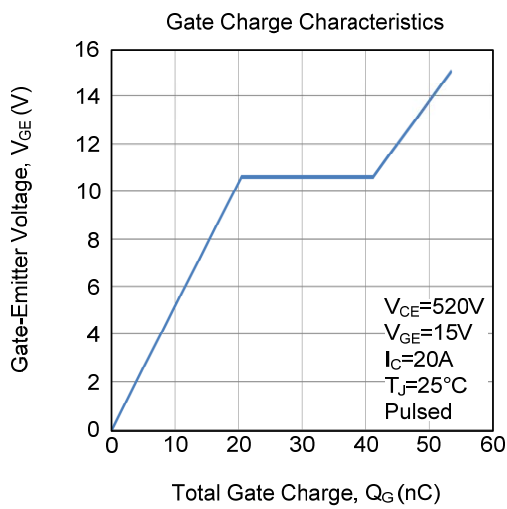
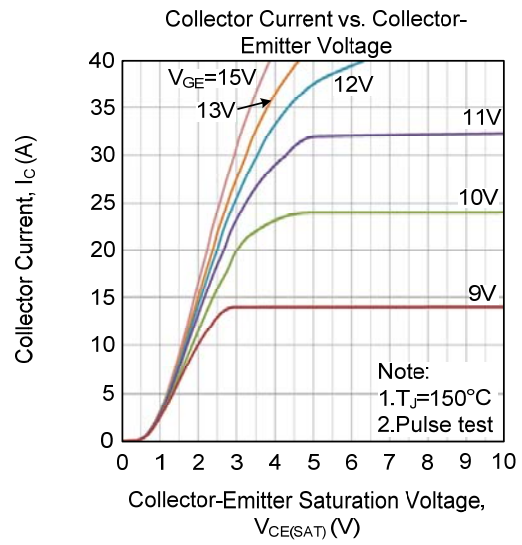
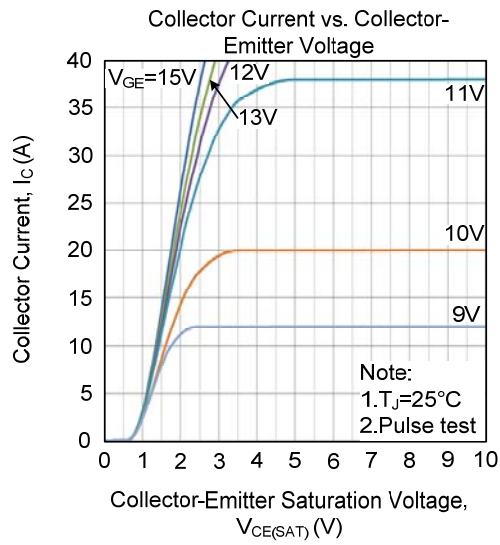
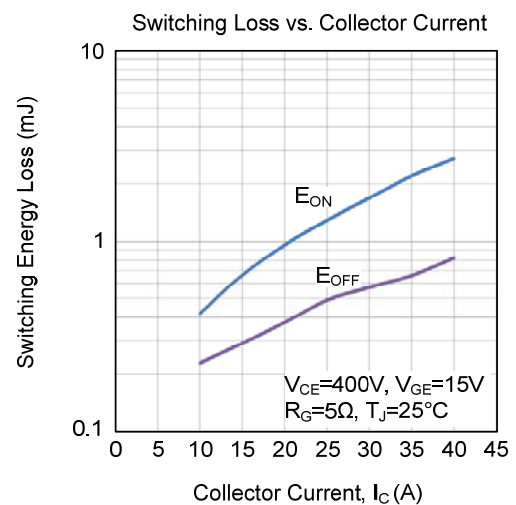
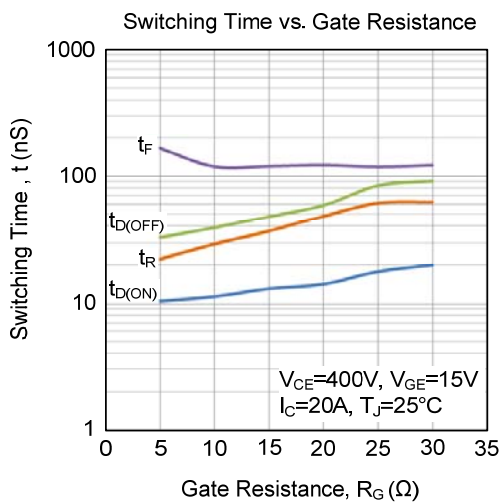
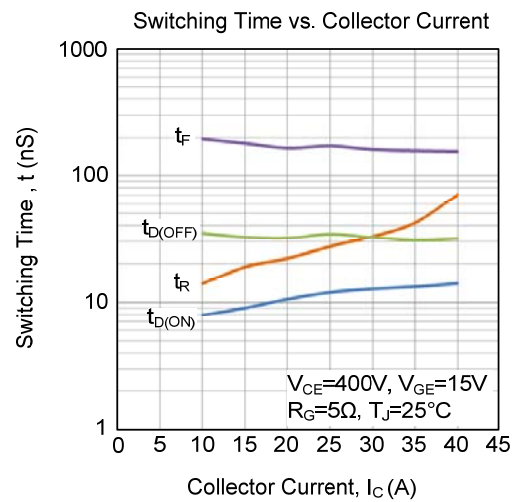
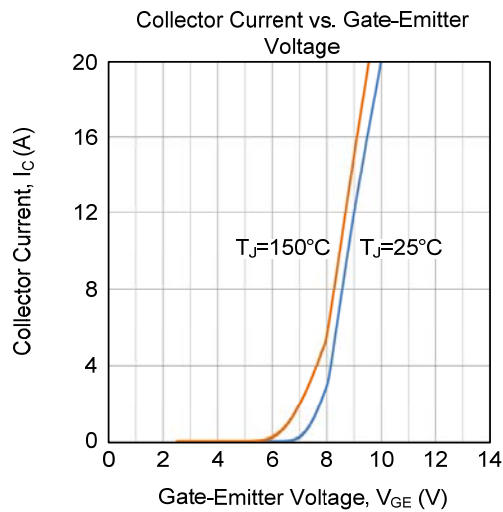
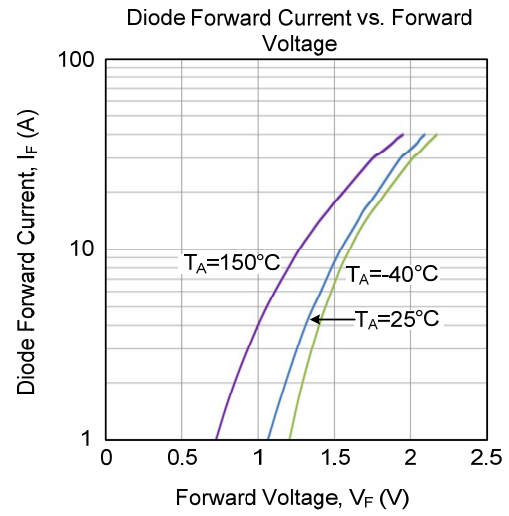
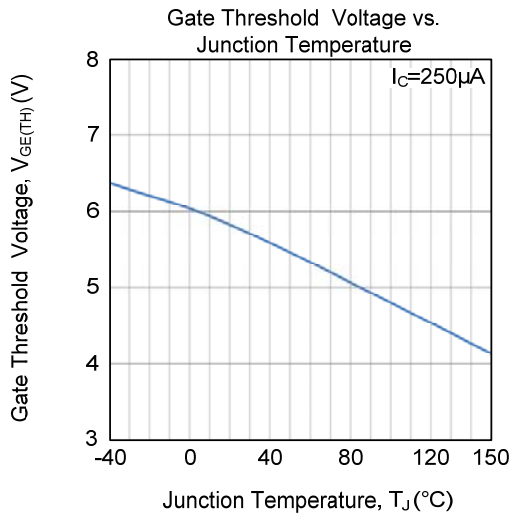


Figure 2. SWITCHING TEST WAVEFORMS

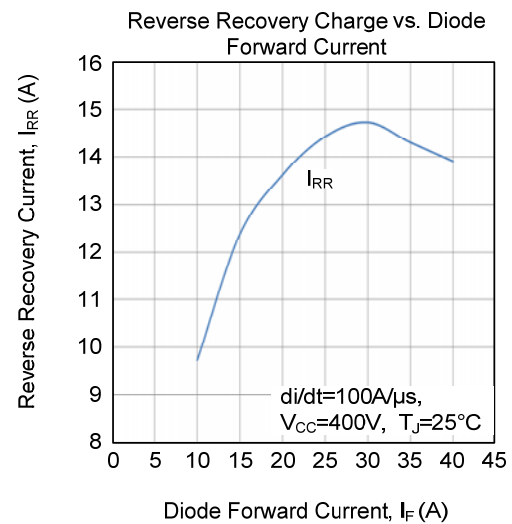
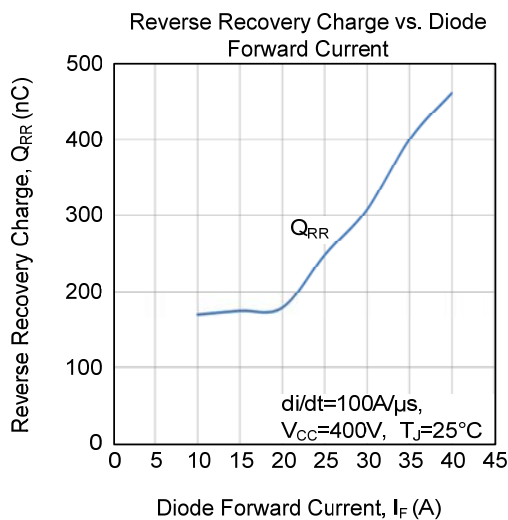
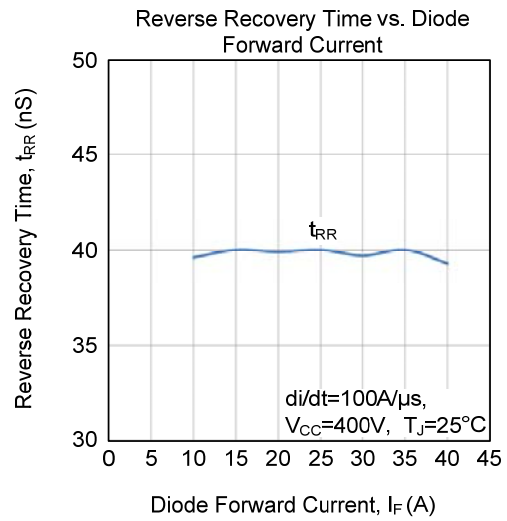
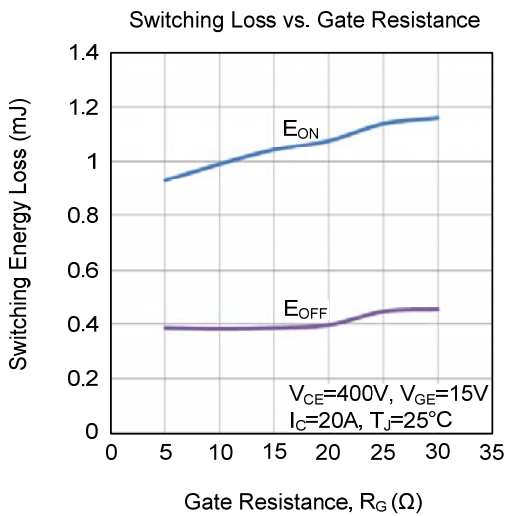
### TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



### ■ TYPICAL CHARACTERISTICS (Cont.)



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