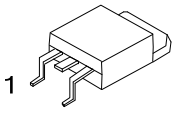
UTG5N65ZLND1 **Preliminary** ***Insulated Gate Bipolar Transistor***

650V TRENCH GATE FIELD-STOP IGBT



TO-252

■ DESCRIPTION

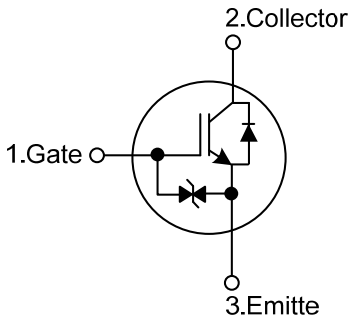
The UTC **UTG5N65ZLND1** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

The UTC **UTG5N65ZLND1** is suitable for the resonant or soft switching applications.

■ FEATURES

* High switching speed
* High avalanche ruggedness
* Low saturation voltage: $V_{CE(SAT).Typ.}$=1.6V @ I_C=5.0A, V_{GE}=15V (T_C =25°C)
* With ESD Protected

■ SYMBOL



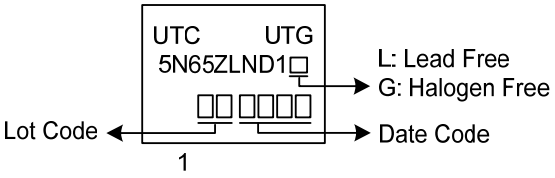
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG5N65ZLND1L-TN3-R	UTG5N65ZLND1G-TN3-R	TO-252	G	C	E	Tape Reel

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UTG5N65ZLND1G-TN3-R (1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING


UTC UTG
5N65ZLND1□
□□□□□□
L: Lead Free
G: Halogen Free
Lot Code
Date Code
1

■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V_{CES}	650	V
Gate-Emitter Voltage		V_{GES}	±20	V
Transient Gate-emitter voltage (t_p < 5 ms)			±25	V
Continuous Collector Current	T_C=25°C	I_C	10	A
	T_C=100°C		5	A
Collector Current Pulsed (Note 1)		I_{CM}	20	A
Diode Forward Current	T_C=25°C	I_F	10	A
	T_C=100°C		5	A
Power Dissipation (T_C=25°C)		P_D	36	W
Operating Junction Temperature		T_J	-40 ~ +150	°C
Storage Temperature Range		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θ_{JC}	3.47	°C/W

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Off Characteristics							
Collector-Emitter Breakdown Voltage	BV_{CES}			650			V
Collector Cut-Off Current	I_{CES}	V_{CE}=650V, V_{GE}=0V				5	μA
G-E Leakage Current	I_{GES}	V_{CE}=0V, V_{GE}=±20V				±10	μA
On Characteristics							
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I_C=250μA, V_{CE}=V_{GE}		4.5		7.5	V
Collector to Emitter Saturation Voltage	$V_{CE(SAT)}$	I_C=5.0A, V_{GE}=15V	T_C=25°C		1.6	2.1	V
			T_C=125°C		2.0		V
Dynamic Characteristics							
Input Capacitance	C_{IES}	V_{CE}=25V, V_{GE}=0V, f=1MHz			490		pF
Output Capacitance	C_{OES}				29		pF
Reverse Transfer Capacitance	C_{RES}				11		pF
Switching Characteristics							
Total Gate Charge	Q_G	V_{CE}=520V, I_C=5.0A, V_{GE}=15V			40		nC
Gate-Emitter Charge	Q_{GE}				15		nC
Gate-Collector Charge	Q_{GC}				15		nC
Turn-On Delay Time	$t_{DON)}$	V_{CC}=400V, I_C=5.0A, R_G=5Ω, V_{GE}=0~15V, L=500μH			5		ns
Rise Time	t_R				12		ns
Turn-Off Delay Time	$t_{DOFF)}$				20		ns
Fall Time	t_F				221		ns
Turn-On Switching Loss	E_{ON}				0.3		mJ
Turn-Off Switching Loss	E_{OFF}				0.1		mJ
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Forward Voltage Drop	V_F	I_F=5.0A				2.0	V
Reverse Recovery Time	t_{rr}	I_F=5.0A, dI/dt=100A/μS,			55		ns
Reverse Recovery Charge	Q_{rr}				237		nC

■ TEST CIRCUIT AND WAVEFORMS

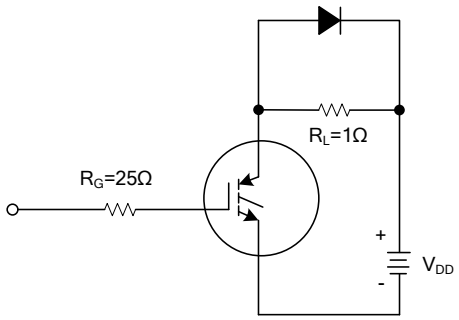


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

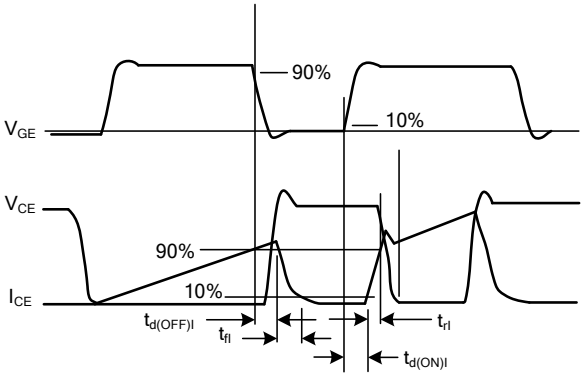


Fig 2. SWITCHING TEST WAVEFORMS