



## ULL2003

Preliminary

LINEAR INTEGRATED CIRCUIT

## 7CH DARLINGTON SINK DRIVER

### ■ DESCRIPTION

The UTC **ULL2003** are high-voltage, high-current darlington drivers comprised of seven NPN Darlington pairs.

All units feature integral clamp diodes for switching inductive loads.

Applications include relay, hammer, lamp and display (LED) drivers.

### ■ FEATURES

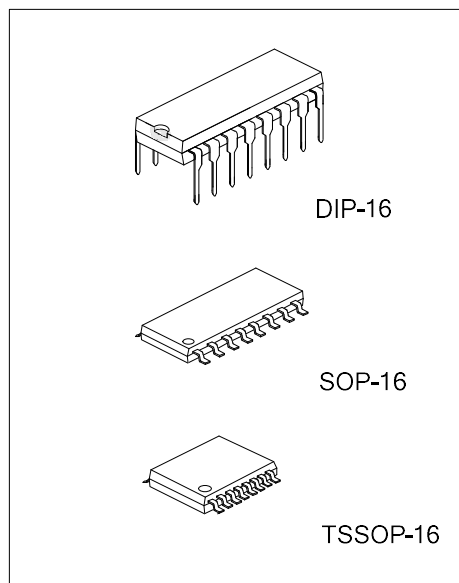
\*Output Current (Single Output): 500mA (Max.)

\*High Sustaining Voltage Output: 50V (Min.)

\*Output Clamp Diodes

\*Inputs Compatible With Various Types Of Logic

\* Green & Pb Free



### ■ ORDERING INFORMATION

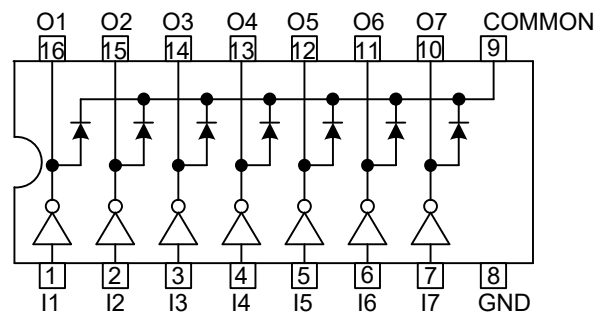
| Ordering Number |                | Package  | Packing   |
|-----------------|----------------|----------|-----------|
| Lead Free       | Halogen Free   |          |           |
| ULL2003L-D16-T  | ULL2003G-D16-T | DIP-16   | Tube      |
| -               | ULL2003G-S16-R | SOP-16   | Tape Reel |
| -               | ULL2003G-P16-R | TSSOP-16 | Tape Reel |

|                                                                                                |                                                                                                                                            |
|------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------|
| <p>ULL2003G-D16-T</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p> | <p>(1) T: Tube, R: Tape Reel</p> <p>(2) D16: DIP-16, S16: SOP-16, P16: TSSOP-16</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p> |
|------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------|

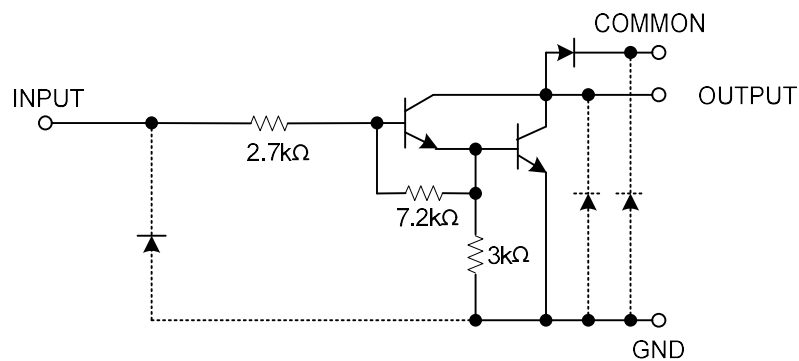
### ■ MARKING

| DIP-16                                                                                                                                                                             | SOP-16 / TSSOP-16                                                                                                                   |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------|
| <p>16 15 14 13 12 11 10 9</p> <p>UTC □□□□</p> <p>ULL2003□</p> <p>□□</p> <p>1 2 3 4 5 6 7 8</p> <p>→ Date Code</p> <p>→ L: Lead Free</p> <p>→ G: Halogen Free</p> <p>→ Lot Code</p> | <p>16 15 14 13 12 11 10 9</p> <p>UTC □□□□</p> <p>ULL2003G</p> <p>□□</p> <p>1 2 3 4 5 6 7 8</p> <p>→ Date Code</p> <p>→ Lot Code</p> |

## ■ PIN CONNECTION



## ■ BLOCK DIAGRAM



Note: The input and output parasitic diodes cannot be used as clamp diodes.

# ■ **ABSOLUTE MAXIMUM RATINGS** ( $T_A=25^{\circ}\text{C}$ , unless otherwise specified)

| PARAMETER                   |          | SYMBOL           | RATING       | UNIT               |
|-----------------------------|----------|------------------|--------------|--------------------|
| Output Sustaining Voltage   |          | $V_{\text{OUT}}$ | -0.5 ~ 50    | V                  |
| Input Voltage               |          | $V_{\text{IN}}$  | -0.5 ~ 30    | V                  |
| Clamp Diode Reverse Voltage |          | $V_{\text{R}}$   | 50           | V                  |
| Output Current              |          | $I_{\text{OUT}}$ | 500          | mA / ch            |
| Clamp Diode Forward Current |          | $I_{\text{F}}$   | 500          | mA                 |
| Power Dissipation           | DIP-16   | $P_{\text{D}}$   | 1.47         | W                  |
|                             | SOP-16   |                  | 1.25 (Note2) | W                  |
|                             | TSSOP-16 |                  | 0.62         | W                  |
| Junction Temperature        |          | $T_{\text{J}}$   | +150         | $^{\circ}\text{C}$ |
| Operating Temperature       |          | $T_{\text{OPR}}$ | -40 ~ +85    | $^{\circ}\text{C}$ |
| Storage Temperature         |          | $T_{\text{STG}}$ | -55 ~ +150   | $^{\circ}\text{C}$ |

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. On PCB (Test Board: JEDEC 2s2p)

# ■ **RECOMMENDED OPERATING CONDITIONS** ( $T_A=-40\sim+85^{\circ}\text{C}$ , unless otherwise specified)

| CHARACTERISTIC              |          | SYMBOL                | TEST CONDITIONS                                                                         |          | MIN | TYP | MAX  | UNIT  |
|-----------------------------|----------|-----------------------|-----------------------------------------------------------------------------------------|----------|-----|-----|------|-------|
| Output Sustaining Voltage   |          | V <sub>OUT</sub>      |                                                                                         |          | 0   |     | 50   | V     |
| Output Current              | DIP-16   | I <sub>OUT</sub>      | T <sub>PW</sub> = 25ms<br>7 Circuits<br>T <sub>A</sub> = 85°C<br>T <sub>J</sub> = 120°C | Duty=10% | 0   |     | 415  | mA/ch |
|                             | SOP-16   |                       |                                                                                         | Duty=50% | 0   |     | 150  |       |
|                             |          |                       |                                                                                         | Duty=10% | 0   |     | 375  |       |
|                             |          |                       |                                                                                         | Duty=50% | 0   |     | 135  |       |
|                             |          |                       |                                                                                         | Duty=10% | 0   |     | 150  |       |
|                             | TSSOP-16 |                       |                                                                                         | Duty=50% | 0   |     | 30   |       |
| Input Voltage               |          | V <sub>IN</sub>       |                                                                                         |          | 0   |     | 24   | V     |
| Input Voltage (Output On)   |          | V <sub>IN (ON)</sub>  | I <sub>OUT</sub> = 400mA                                                                |          | 2.8 |     | 24   | V     |
| Input Voltage (Output Off)  |          | V <sub>IN (OFF)</sub> |                                                                                         |          | 0   |     | 0.7  | V     |
| Clamp Diode Reverse Voltage |          | V <sub>R</sub>        |                                                                                         |          |     |     | 50   | V     |
| Clamp Diode Forward Current |          | I <sub>F</sub>        |                                                                                         |          |     |     | 350  | mA    |
| Power Dissipation           | DIP-16   | P <sub>D</sub>        | T <sub>A</sub> = 85°C                                                                   |          |     |     | 0.76 | W     |
|                             | SOP-16   |                       | T <sub>A</sub> = 85°C (Note)                                                            |          |     |     | 0.65 | W     |
|                             | TSSOP-16 |                       | T <sub>A</sub> = 85°C                                                                   |          |     |     | 0.32 | W     |

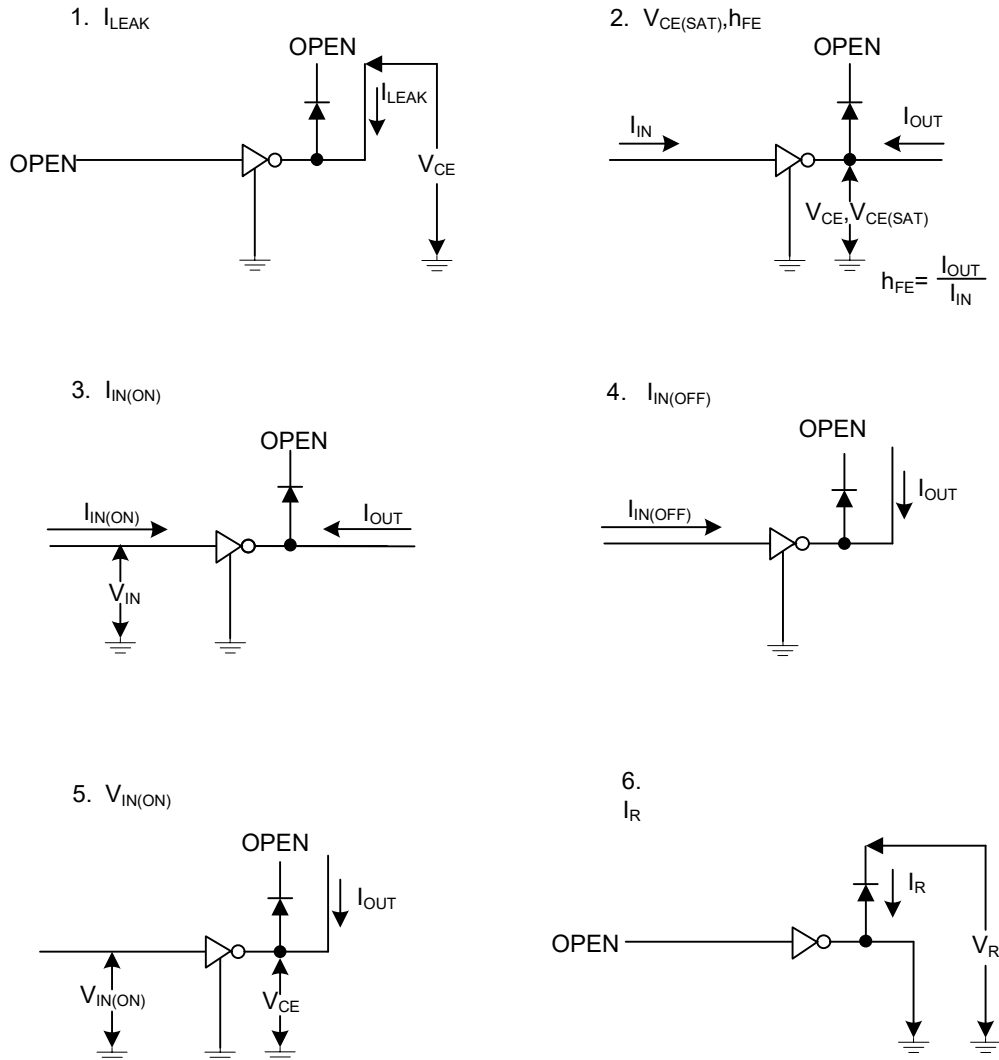
Note: On PCB (Test Board: JEDEC 2s2p)

■ **ELECTRICAL CHARACTERISTICS** ( $T_A=25^\circ\text{C}$ , unless otherwise specified)

| CHARACTERISTIC                       | SYMBOL        | TEST CIRCUIT | TEST CONDITIONS                                             | MIN  | TYP | MAX | UNIT          |
|--------------------------------------|---------------|--------------|-------------------------------------------------------------|------|-----|-----|---------------|
| Output Leakage Current               | $I_{LEAK}$    | 1            | $V_{CE}=50\text{V}$ , $T_A=25^\circ\text{C}$                |      |     | 50  | $\mu\text{A}$ |
|                                      |               |              | $V_{CE}=50\text{V}$ , $T_A=85^\circ\text{C}$                |      |     | 100 | $\mu\text{A}$ |
| Collector-Emitter Saturation Voltage | $V_{CE(SAT)}$ | 2            | $I_{OUT}=350\text{mA}$ , $I_{IN}=500\mu\text{A}$            |      | 1.3 | 1.6 | V             |
|                                      |               |              | $I_{OUT}=200\text{mA}$ , $I_{IN}=350\mu\text{A}$            |      | 1.1 | 1.3 | V             |
|                                      |               |              | $I_{OUT}=100\text{mA}$ , $I_{IN}=250\mu\text{A}$            |      | 0.9 | 1.1 | V             |
| DC Current Transfer Ratio            | $h_{FE}$      | 2            | $V_{CE}=2\text{V}$ , $I_{OUT}=350\text{mA}$                 | 1000 |     |     |               |
| Input Current (Output On)            | $I_{IN(ON)}$  | 3            | $V_{IN}=2.4\text{V}$ , $I_{OUT}=350\text{mA}$               |      | 0.4 | 0.7 | mA            |
| Input Current (Output Off)           | $I_{IN(OFF)}$ | 4            | $I_{OUT}=500\mu\text{A}$ , $T_A=85^\circ\text{C}$           | 50   | 65  |     | $\mu\text{A}$ |
| Input Voltage (Output On)            | $V_{IN(ON)}$  | 5            | $V_{CE}=2\text{V}$                                          |      |     | 2.6 | V             |
|                                      |               |              | $I_{OUT} = 350\text{ mA}$                                   |      |     | 2.0 | V             |
| Clamp Diode Reverse Current          | $I_R$         | 6            | $V_R=50\text{V}$ , $T_A=25^\circ\text{C}$                   |      |     | 50  | $\mu\text{A}$ |
|                                      |               |              | $V_R=50\text{V}$ , $T_A=85^\circ\text{C}$                   |      |     | 100 | $\mu\text{A}$ |
| Clamp Diode Forward Voltage          | $V_F$         | 7            | $I_F=350\text{mA}$                                          |      |     | 2.0 | V             |
| Input Capacitance                    | $C_{IN}$      |              |                                                             |      | 15  |     | pF            |
| Turn-On Delay                        | $t_{ON}$      | 8            | $V_{OUT}=50\text{V}$ , $R_L=125\Omega$<br>$C_L=15\text{pF}$ |      | 0.1 |     | $\mu\text{s}$ |
| Turn-Off Delay                       | $t_{OFF}$     | 8            | $V_{OUT}=50\text{V}$ , $R_L=125\Omega$<br>$C_L=15\text{pF}$ |      | 0.2 |     | $\mu\text{s}$ |

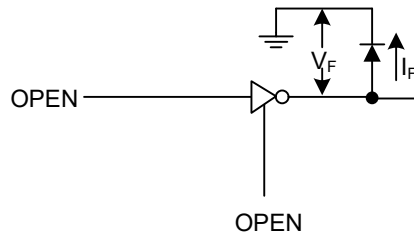
## ■ TEST CIRCUIT

Components in the test circuits are used only to obtain and confirm the device characteristics. These components and circuits are not guaranteed to prevent malfunction or failure from occurring in the application equipment.

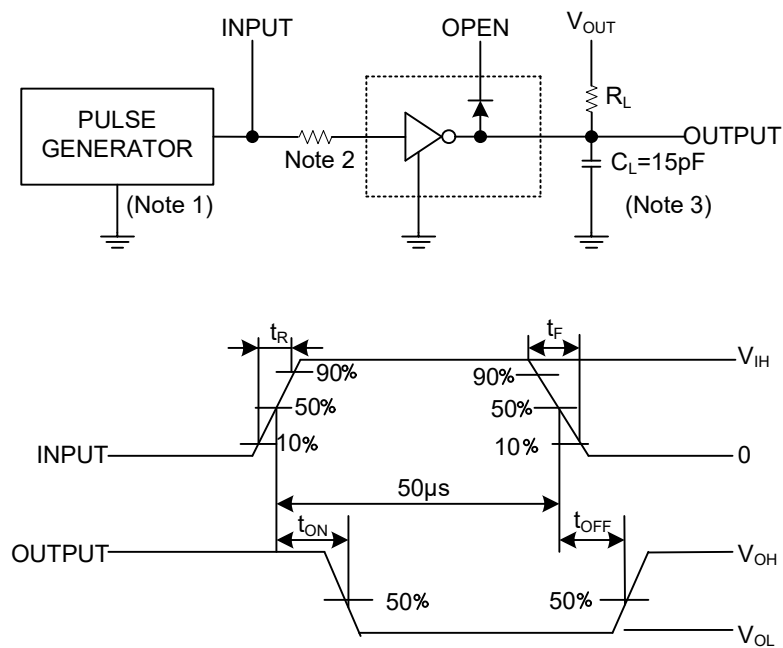


# ■ TEST CIRCUIT (Cont.)

7.  $V_F$



8.  $t_{ON}$ ,  $t_{OFF}$



Notes: 1. Pulse width 50μs, duty cycle 10%, Output impedance 50Ω,  $t_R \leq 5\text{ns}$ ,  $t_F \leq 10\text{ns}$ .

2. See below.

## INPUT CONDITION

| TYPE NUMBER | R1 | $V_{IH}$ |
|-------------|----|----------|
| ULL2003     | 0  | 3V       |

3:  $C_L$  includes probe and jig capacitance.

**■ APPLICATION NOTES**

This UTC **ULL2003** has no protection functions for over-current and over-voltage. If the applied situation includes over-current or over-voltage, this IC may be destroyed. We should take care of the systems design with this IC. The OUTPUT, COMMON and GND lines should be designed carefully, because the IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

The UTC **ULL2003** is a darlington driver array. When it is applied with a general logic circuit such as TTL or CMOS, resistors should be connected in series to every input to achieve a stable input current.

When the load is connected between the output and the power supply, the COMMON pin should be shorted to the power supply to protect the IC from extra swing voltage.

**■ NOTICE ON PRODUCT USE**

(1) The absolute maximum ratings should be not exceeded, even for a moment, otherwise the device may be breakdown, damaged or deteriorated, and even injury by explosion or combustion.

(2) The power supply fuse should be used to prevent a continuous large current from the over current situation and IC failure. The improper wiring layout and an abnormal pulse noise from the wiring or load both can cause a large continuous current and even the breakdown, and finally can lead smoke or ignition. So we should design carefully considering fuse capacity, fusing time and insertion circuit location, the effects of a large current can be improved.

(3) When the inductive load is applied such as motor coil, it can cause the device malfunction or breakdown due to the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF, so a protection circuit should be designed. When using build-in protection functions IC, a stable power supply is required, otherwise the protection function may not operate and cause IC breakdown. IC breakdown may result injury, smoke or ignition.

(4) The devices should not be mounted in the wrong orientation or incorrectly, and the positive and negative terminals of power supplies should also be connected properly, otherwise the current or power consumption may exceed the absolute maximum rating, also it may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion. In addition, any device should not be used when it is applied the current with mounted in the wrong orientation or incorrectly even just one time.

(5) The careful selection of external components is also important, such as inputs and negative feedback capacitors, load components (such as speakers), power amp and regulator. If a large leakage current flow the input or negative feedback capacitors, the IC output DC voltage will be increased. If this output voltage is connected to a speaker with low input withstand voltage, the smoke or ignition can be caused for over-current or IC failure, even the smoke or ignition from the IC itself. In addition, please also pay attention to the Bridge Tied Load (BTL) connection type IC that inputs output DC voltage to a speaker directly.

**■ POINTS TO REMEMBER ON PRODUCT USE****(1) Heat Dissipation Design**

An inadequate IC heat dissipation design can reduce the IC life, deterioration of IC characteristics, even IC breakdown. When the IC is applied with a large current flow such as power amp, regulator and so on, the careful heat dissipation design should be implemented, not to exceed the maximum ratings of junction temperature ( $T_j$ ). Meanwhile, also please pay attention to the effect of IC heat dissipation on peripheral components.

**(2) Back-EMF**

Being the effect of back-EMF, a current can flow back to the motor's power supply when a motor rotates in the reverse direction, stops or slows down suddenly. If the power supply has a low current sink capability, the device's motor power supply and output pins might be influenced for exceeding the absolute maximum ratings. In the system design, we should take the effect of back-EMF into consideration to avoid the accident.

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