



UTG75N65LND1

Advance

Insulated Gate Bipolar Transistor

650V TRENCH GATE FIELD-STOP IGBT

DESCRIPTION

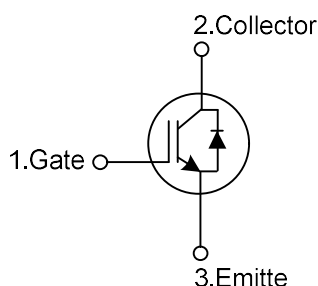
The UTC **UTG75N65LND1** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

The UTC **UTG75N65LND1** is suitable for the resonant or soft switching applications.

FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: $V_{CE(SAT).Typ.}=1.7V$ @ $I_C=75A$, $V_{GE}=15V$ ($T_C=25^{\circ}C$)

SYMBOL

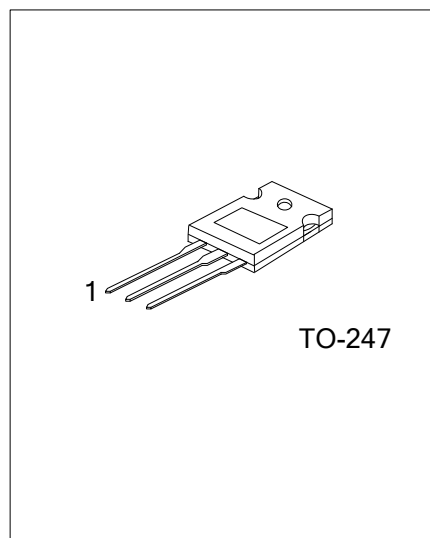


ORDERING INFORMATION

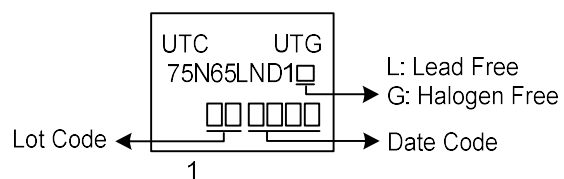
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG75N65LND1L-T47-T	UTG75N65LND1G-T47-T	TO-247	G	C	E	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UTG75N65LND1G-T47-T	(1)Packing Type	(1) T: Tube
	(2)Package Type	(2) T47: TO-247
	(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V _{CES}	650	V
Gate-Emitter Voltage		V _{GES}	±20	V
Transient Gate-emitter voltage (tp < 5 ms)			±25	V
Continuous Collector Current	Tc=25°C	Ic	150	A
	Tc=100°C		75	A
Collector Current Pulsed (Note 1)		ICM	300	A
Diode Forward Current	Tc=25°C	If	150	A
	Tc=100°C		75	A
Short Circuit Withstand Time VGE = 15V, VCC ≤ 200V Allowed number of short circuits < 1000 Time between short circuits: ≥1.0s TVJ= 25°C		tsc	8	μs
Power Dissipation (Tc=25°C)		PD	310	W
Operating Junction Temperature		TJ	-40 ~ +150	°C
Storage Temperature Range		TSTG	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Absolute maximum ratings are those values beyond which the device could be permanently damaged.

2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θ _{JC}	0.4	°C/W

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Off Characteristics						
Collector-Emitter Breakdown Voltage	BV _{CES}		650			V
Collector Cut-Off Current	I _{CES}	V _{CE} =650V, V _{GE} =0V			5	μA
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V			±400	nA
On Characteristics						
Gate to Emitter Threshold Voltage	V _{GE(TH)}	I _C =250μA, V _{CE} =V _{GE}	4.5		7.5	V
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _C =75A, V _{GE} =15V	T _C =25°C	1.7	2.2	V
			T _C =125°C	2.1		V
Dynamic Characteristics						
Input Capacitance	C _{IES}	V _{CE} =25V, V _{GE} =0V, f=1MHz		3		nF
Output Capacitance	C _{OES}			287		pF
Reverse Transfer Capacitance	C _{RES}			59		pF
Switching Characteristics						
Total Gate Charge	Q _G	V _{CE} =520V, I _C =75A, V _{GE} =15V		131		nC
Gate-Emitter Charge	Q _{GE}			38		nC
Gate-Collector Charge	Q _{GC}			64		nC
Turn-On Delay Time	t _{DON})	V _{CC} =400V, I _C =75A, R _G =5Ω, V _{GE} =0~15V, L=500μH		25		ns
Rise Time	t _R			107		ns
Turn-Off Delay Time	t _{DOFF})			100		ns
Fall Time	t _F			47		ns
Turn-On Switching Loss	E _{ON}			4.5		mJ
Turn-Off Switching Loss	E _{OFF}			2.2		mJ
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Forward Voltage Drop	V _F	I _F =75A			2.1	V
Reverse Recovery Time	t _{rr}	I _F =75A, dI/dt=100A/μS,		69		ns
Reverse Recovery Charge	Q _{rr}	V _{CC} =600V		1437		μC

■ TEST CIRCUIT AND WAVEFORMS

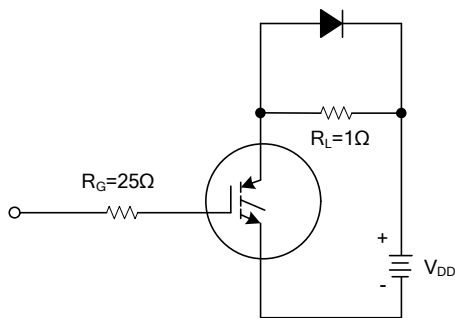


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

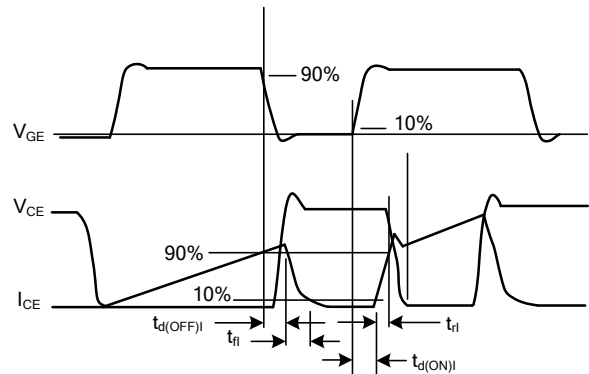


Fig 2. SWITCHING TEST WAVEFORMS

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