



UTG40N65LLS1

Insulated Gate Bipolar Transistor

650V TRENCH GATE FIELD-STOP IGBT

DESCRIPTION

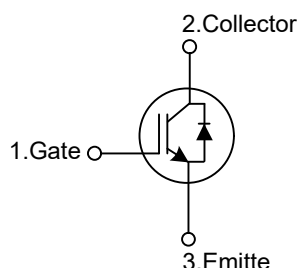
The UTC **UTG40N65LLS1** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

The UTC **UTG40N65LLS1** is suitable for the resonant or soft switching applications.

FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: $V_{CE(SAT).Typ.}=1.49V$ @ $I_C=40A$, $V_{GE}=15V$ ($T_C=25^{\circ}C$)

SYMBOL

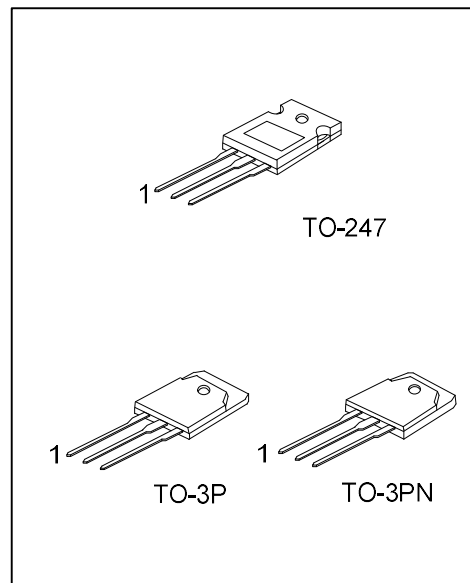


ORDERING INFORMATION

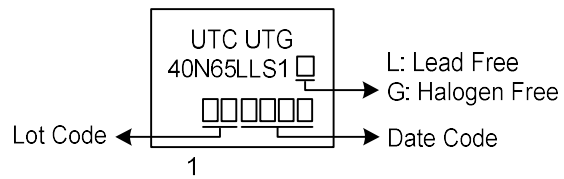
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG40N65LLS1L-T47-T	UTG40N65LLS1G-T47-T	TO-247	G	C	E	Tube
UTG40N65LLS1L-T3P-T	UTG40N65LLS1G-T3P-T	TO-3P	G	C	E	Tube
UTG40N65LLS1L-T3N-T	UTG40N65LLS1G-T3N-T	TO-3PN	G	C	E	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UTG40N65LLS1G-T47-T	(1)Packing Type	(1) T: Tube
	(2)Package Type	(2) T47: TO-247, T3P: TO-3P, T3N: TO-3PN
	(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^{\circ}\text{C}$, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V_{CES}	650	V
Gate-Emitter Voltage		V_{GES}	± 20	V
Transient Gate-emitter voltage ($t_p < 5\text{ ms}$)			± 25	V
Continuous Collector Current	$T_C=25^{\circ}\text{C}$	I_C	80	A
	$T_C=100^{\circ}\text{C}$		40	A
Collector Current Pulsed (Note 1)		I_{CM}	160	A
Diode Forward Current	$T_C=25^{\circ}\text{C}$	I_F	80	A
	$T_C=100^{\circ}\text{C}$		40	A
Short Circuit Withstand Time $V_{GE} = 15\text{V}$, $V_{CC} \leq 200\text{V}$ Allowed number of short circuits < 1000 Time between short circuits: $\geq 1.0\text{s}$ $T_{VJ}= 25^{\circ}\text{C}$		t_{SC}	8	μs
Power Dissipation ($T_C=25^{\circ}\text{C}$)	TO-247	P_D	298	W
	TO-3P/TO-3PN		320	W
Operating Junction Temperature		T_J	$-40 \sim +150$	$^{\circ}\text{C}$
Storage Temperature Range		T_{STG}	$-55 \sim +150$	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Absolute maximum ratings are those values beyond which the device could be permanently damaged.

2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Case	TO-247	θ_{JC}	0.41	$^{\circ}\text{C/W}$
	TO-3P/TO-3PN		0.39	$^{\circ}\text{C/W}$

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Off Characteristics							
Collector-Emitter Breakdown Voltage	BV _{CES}			650			V
Collector Cut-Off Current	I _{CES}	V _{CE} =650V, V _{GE} =0V				5	μA
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V				±400	nA
On Characteristics							
Gate to Emitter Threshold Voltage	V _{GE(TH)}	I _C =250μA, V _{CE} =V _{GE}		3.5		6.5	V
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _C =40A, V _{GE} =15V	T _C =25°C		1.49	2	V
			T _C =125°C		1.7		V
Dynamic Characteristics							
Input Capacitance	C _{IES}	V _{CE} =25V, V _{GE} =0V, f=1MHz			2350		pF
Output Capacitance	C _{OES}				190		pF
Reverse Transfer Capacitance	C _{RES}				51		pF
Switching Characteristics							
Total Gate Charge	Q _G	V _{CE} =520V, I _C =40A, V _{GE} =15V			110		nC
Gate-Emitter Charge	Q _{GE}				27		nC
Gate-Collector Charge	Q _{GC}				52		nC
Turn-On Delay Time	t _{DON}	V _{CC} =400V, I _C =40A, R _G =5Ω, V _{GE} =0~15V, L=500uH			16		ns
Rise Time	t _R				36		ns
Turn-Off Delay Time	t _{DOFF}				81		ns
Fall Time	t _F				127		ns
Turn-On Switching Loss	E _{ON}				1.53		mJ
Turn-Off Switching Loss	E _{OFF}				1.11		mJ
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Forward Voltage Drop	V _F	I _F =40A				2.1	V
Reverse Recovery Time	t _{rr}	I _F =40A, dI/dt=100A/μS, V _{CC} =400V			42		ns
Reverse Recovery Charge	Q _{rr}				77		nC

■ TEST CIRCUIT AND WAVEFORMS

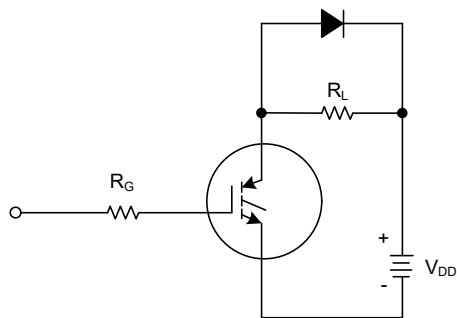


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

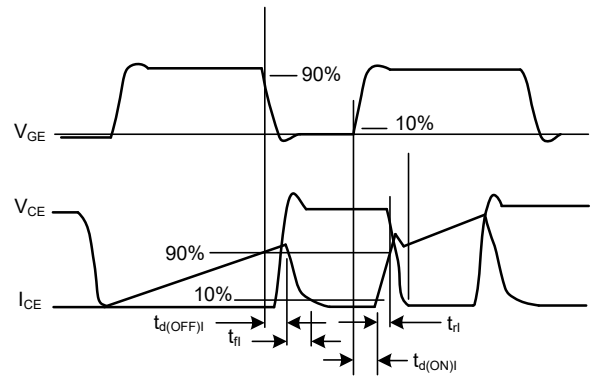
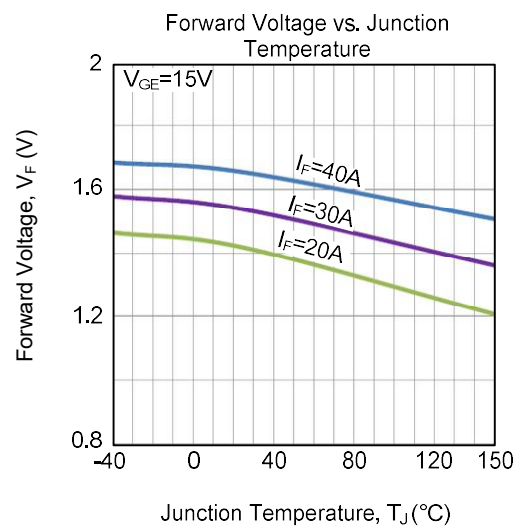
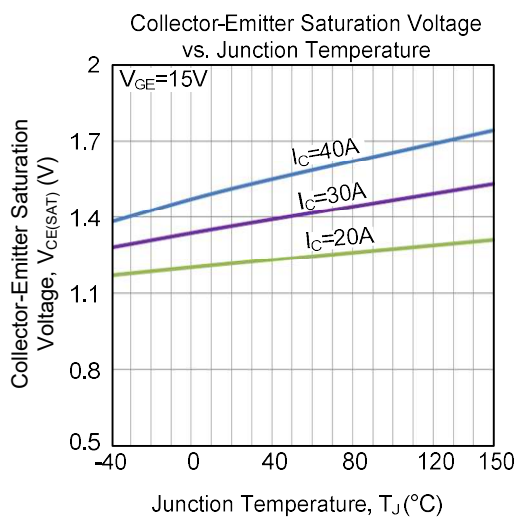
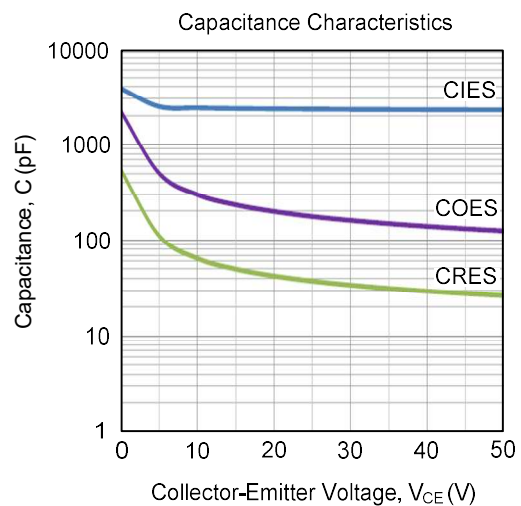
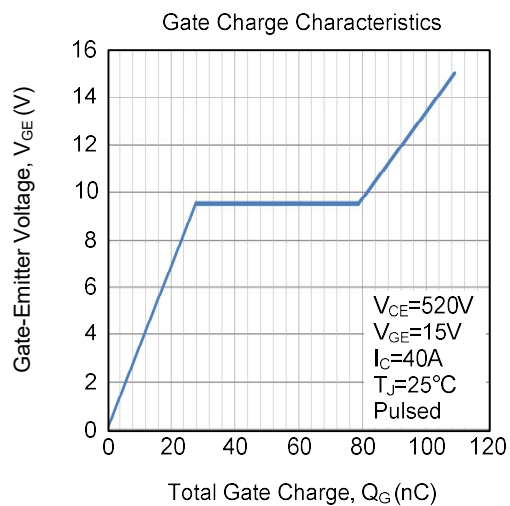
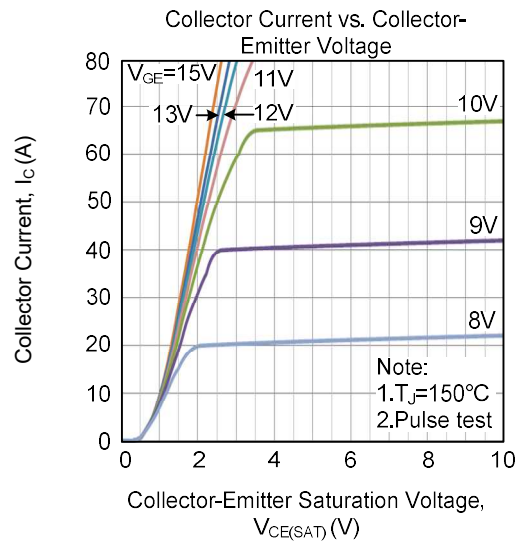
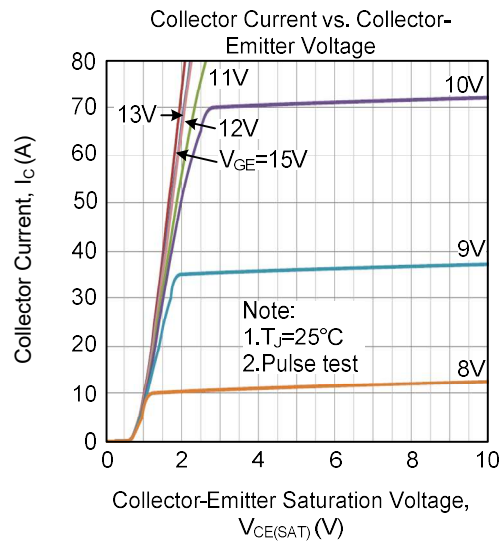
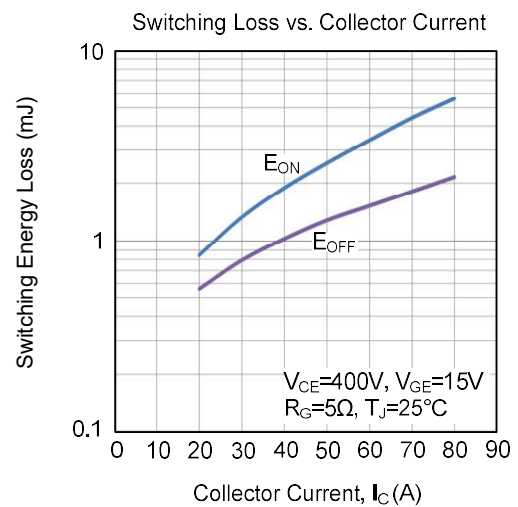
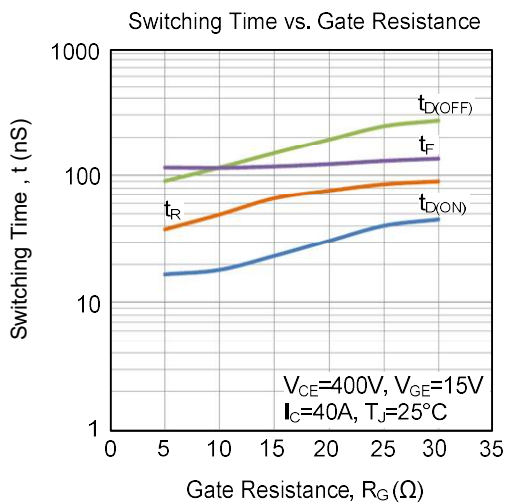
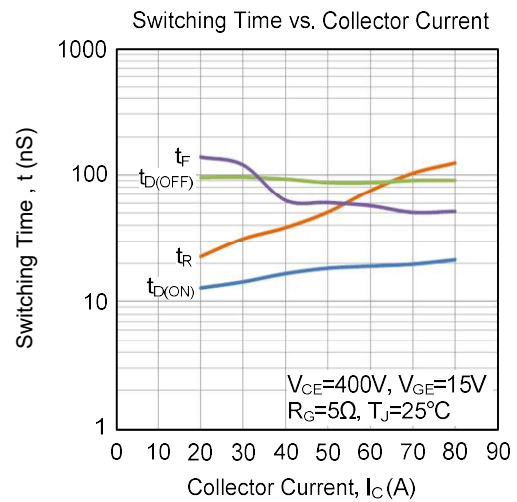
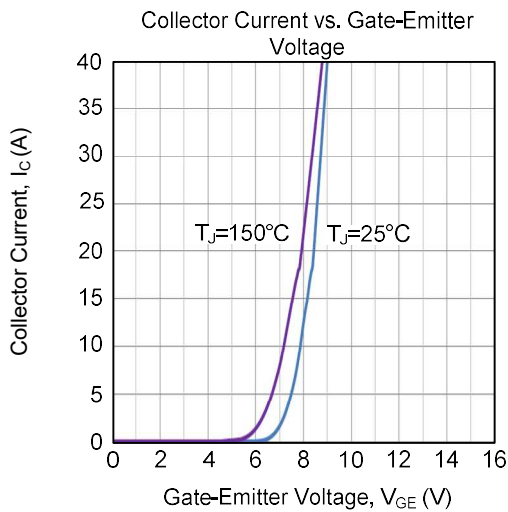
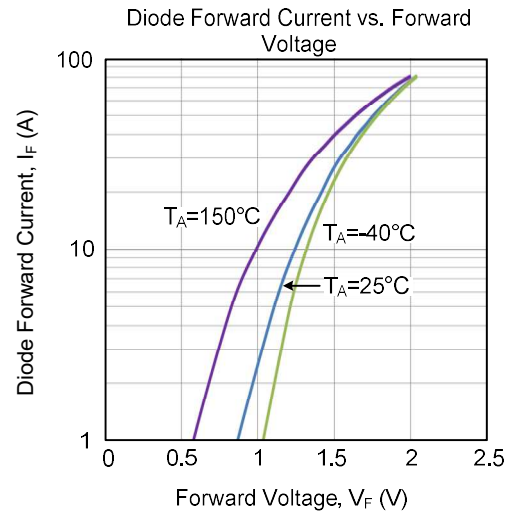
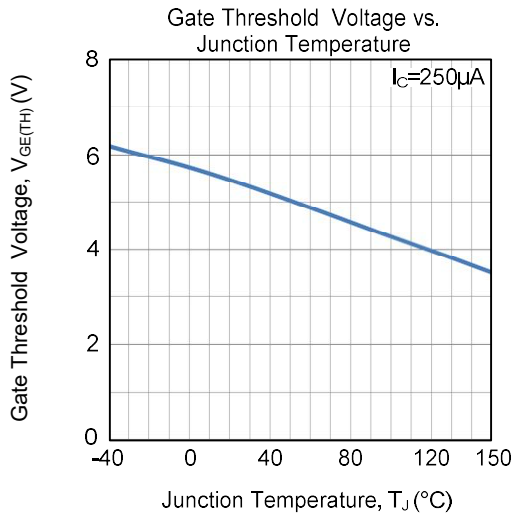


Fig 2. SWITCHING TEST WAVEFORMS

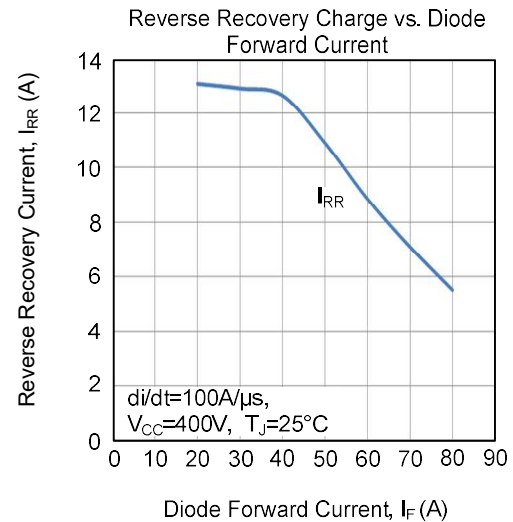
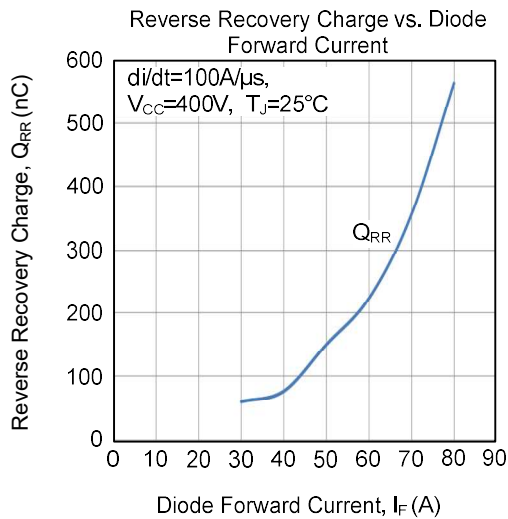
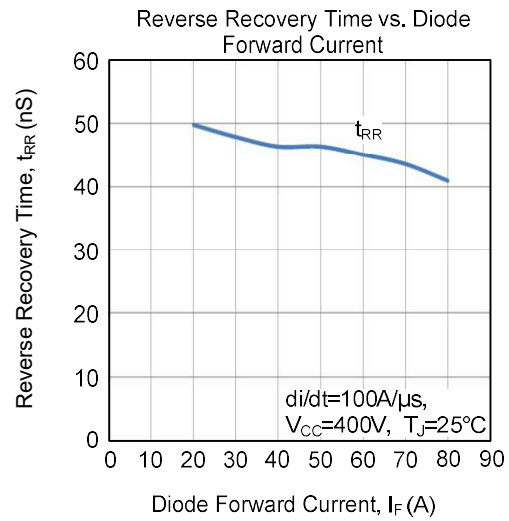
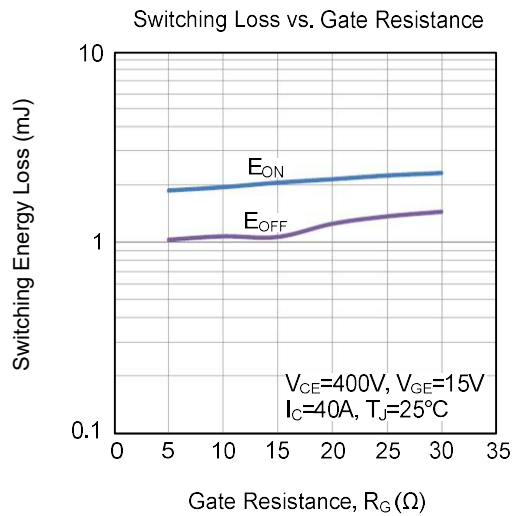
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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