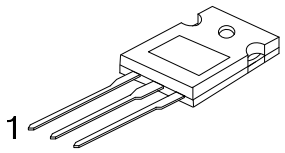
UTG50N65LX1

Insulated Gate Bipolar Transistor

650V TRENCH GATE FIELD-STOP IGBT



TO-247

■ DESCRIPTION

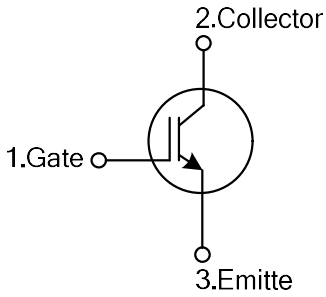
The UTC **UTG50N65LX1** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

The UTC **UTG50N65LX1** is suitable for the resonant or soft switching applications.

■ FEATURES

* High switching speed
* High avalanche ruggedness
* Low saturation voltage: $V_{CE(SAT).Typ.}$=1.8V @ I_C=50A, V_{GE}=15V (T_C =25°C)

■ SYMBOL



■ ORDERING INFORMATION

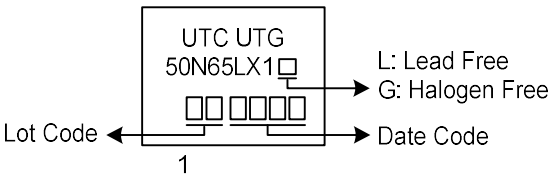
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG50N65LX1L-T47-T	UTG50N65LX1G-T47-T	TO-247	G	C	E	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UTG50N65LX1G-T47-T (1)Packing Type	(1) T: Tube
(2)Package Type	(2) T47: TO-247
(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

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■ MARKING


UTC UTG
50N65LX1□
L: Lead Free
G: Halogen Free
Lot Code
Date Code
1

■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V_{CES}	650	V
Gate-Emitter Voltage		V_{GES}	±20	V
Transient Gate-emitter voltage (t_p < 5 ms)			±25	V
Continuous Collector Current	T_C=25°C	I_C	100	A
	T_C=100°C		50	A
Collector Current Pulsed (Note 1)		I_{CM}	200	A
Diode Forward Current	T_C=25°C	I_F	100	A
	T_C=100°C		50	A
Short Circuit Withstand Time V_{GE} = 15V, V_{CC} ≤ 200V Allowed number of short circuits < 1000 Time between short circuits: ≥1.0s T_{VJ}= 25°C		t_{SC}	8	μs
Power Dissipation (T_C=25°C)		P_D	296	W
Operating Junction Temperature		T_J	-40 ~ +150	°C
Storage Temperature Range		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θ_{JC}	0.42	°C/W

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Off Characteristics							
Collector-Emitter Breakdown Voltage	BV_{CES}			650			V
Collector Cut-Off Current	I_{CES}	V_{CE}=650V, V_{GE}=0V				5	μA
G-E Leakage Current	I_{GES}	V_{CE}=0V, V_{GE}=±20V				±400	nA
On Characteristics							
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I_C=250μA, V_{CE}=V_{GE}		4.5		7.5	V
Collector to Emitter Saturation Voltage	$V_{CE(SAT)}$	I_C=50A, V_{GE}=15V	T_C=25°C		1.8	2.3	V
			T_C=125°C		2.2		V
Dynamic Characteristics							
Input Capacitance	C_{IES}	V_{CE}=25V, V_{GE}=0V, f=1MHz			1.64		nF
Output Capacitance	C_{OES}				117		pF
Reverse Transfer Capacitance	C_{RES}				39		pF
Switching Characteristics							
Total Gate Charge	Q_G	V_{CE}=520V, I_C=50A, V_{GE}=15V			83		nC
Gate-Emitter Charge	Q_{GE}				27		nC
Gate-Collector Charge	Q_{GC}				37		nC
Turn-On Delay Time	$t_{DON)}$	V_{CC}=400V, I_C=50A, R_G=5Ω, V_{GE}=0~15V, L=500μH			16		ns
Rise Time	t_R				60		ns
Turn-Off Delay Time	$t_{DOFF)}$				66		ns
Fall Time	t_F				51		ns
Turn-On Switching Loss	E_{ON}				2.7		mJ
Turn-Off Switching Loss	E_{OFF}				1.2		mJ

■ TEST CIRCUIT AND WAVEFORMS

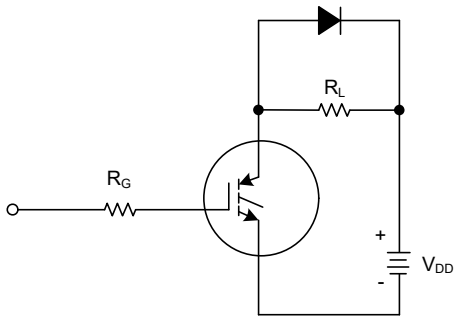


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

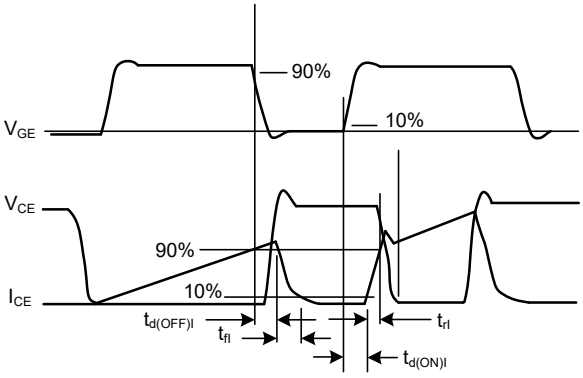
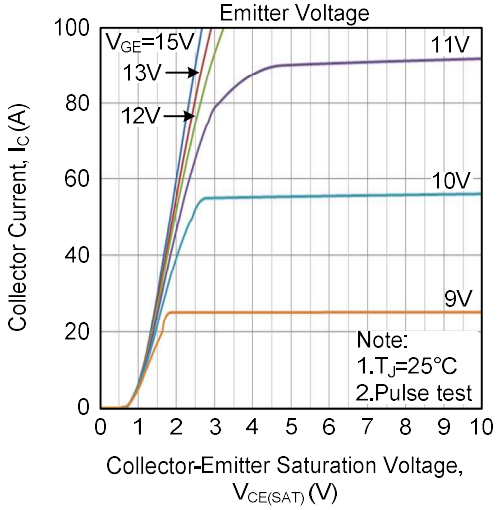


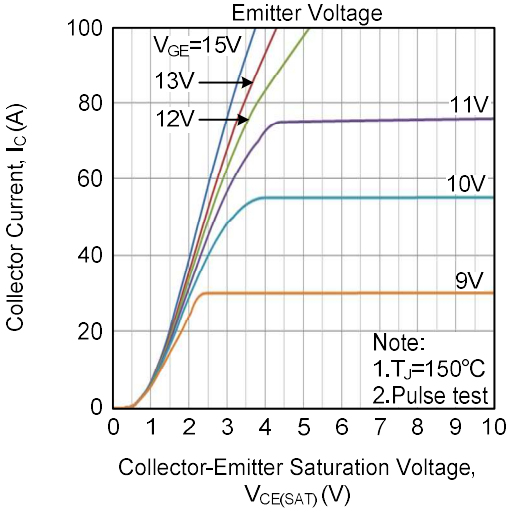
Fig 2. SWITCHING TEST WAVEFORMS

■ TYPICAL CHARACTERISTICS

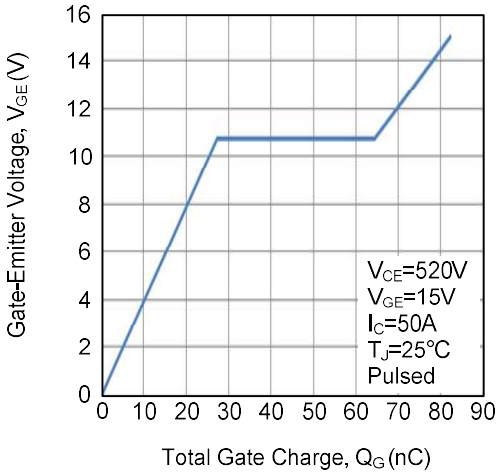
Collector Current vs. Collector-Emitter Voltage



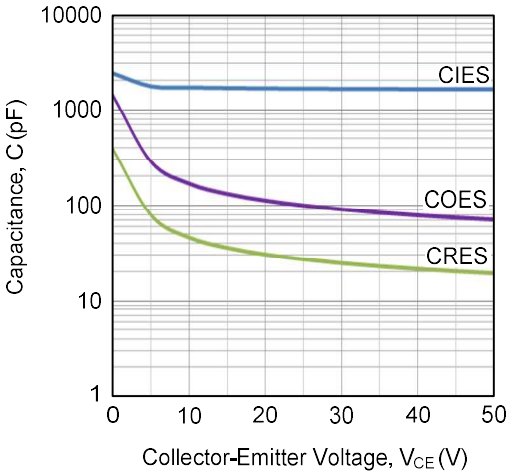
Collector Current vs. Collector-Emitter Voltage



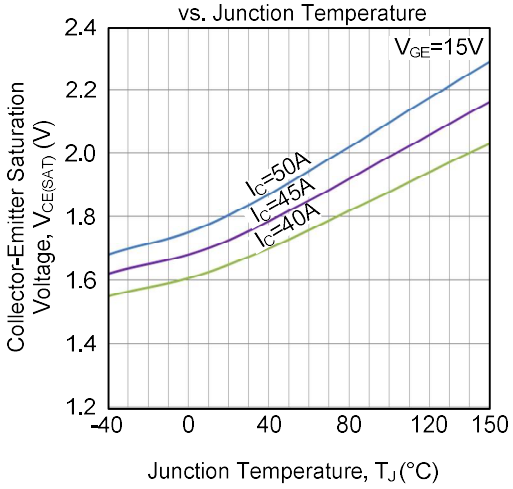
Gate Charge Characteristics



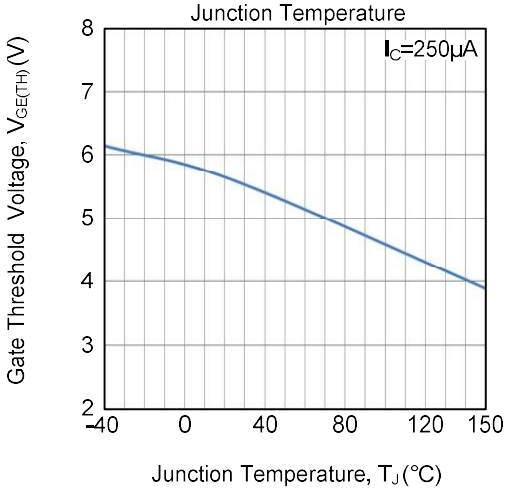
Capacitance Characteristics



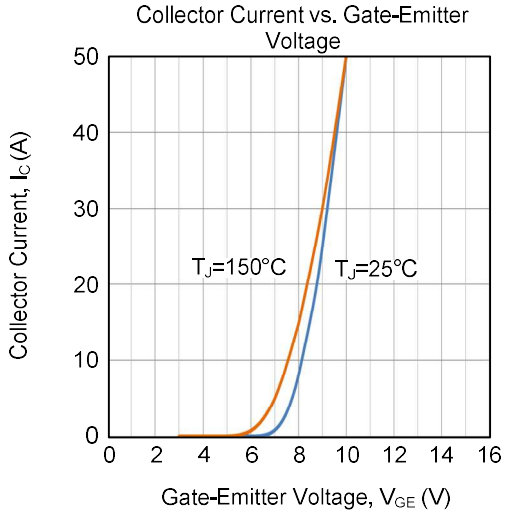
Collector-Emitter Saturation Voltage vs. Junction Temperature

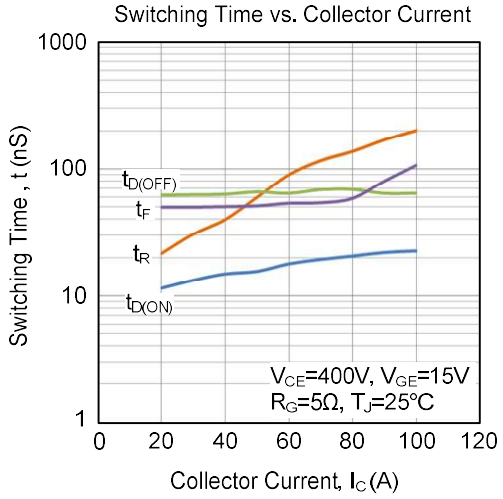


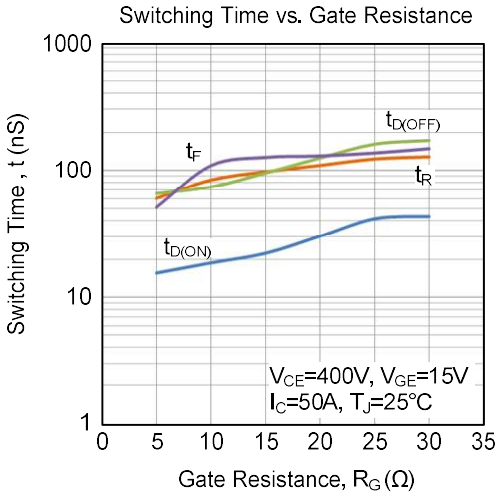
Gate Threshold Voltage vs. Junction Temperature

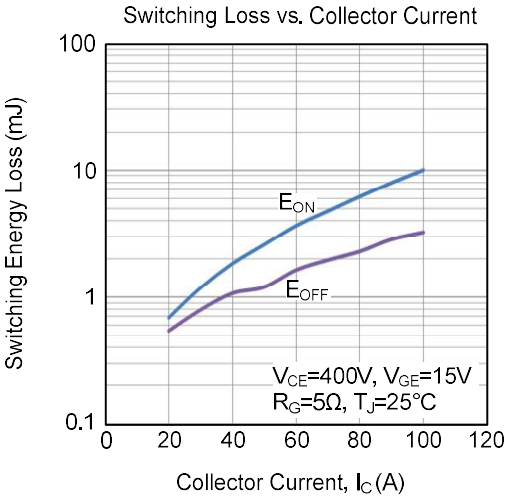


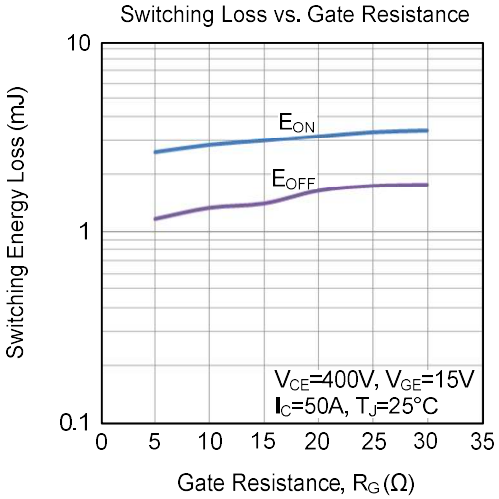
■ TYPICAL CHARACTERISTICS (Cont.)











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